

6800 Terminal Card  
Software



53-110

Made in U.S.A.



6800 Software for the Terminal Card

# I/O Registers

0		Display Address Register	<15:8>			18
1		Display Address Register	<7:0>			19
2	P	Control Register A	[CA1]	INIT	<u>IRQ1</u>	1A
3	P	Control Register B	---			10
4		Cursor Address Register	<15:8>			1C
5		Cursor Address Register	<7:0>			1D
6	P	Control Register A	---			1E
7	P	Control Register B	[CB1]	Vertical Retrace	<u>NMI2</u>	1F
8		Current Scan Line				20
A		Interrupt Scan Line				21
B	P	Control Register A	---			22
C	P	Control Register B	[CB1]	Scan Line	<u>NMI3</u>	23
D		Characters per Line				24
E		Display Control				25
F	P	Control Register A	[NMI Stroke]			26
	P	Control Register B	[CB1]	ODD FRAME	<u>NMI1</u>	27
10		Word Data from BUS	<15:08>			28
11		Word Data from BUS	<7:0>			29
12	P	Control Register A	[CA1/CA2]	Data Ready	<u>IRQ8</u>	2A
13	P	Control Register B	---			2B
14		Word Data to BUS	<15:07>			2C
15		Word Data to BUS	<7:0>			2D
16	P	Control Register A				2E
17	P	Control Register B	[CB1/02]	Data Ready	<u>IRQ9</u>	2F



# I/O Registers

18	30		Terminal Data from II		
19	31		Terminal Data to II		
1A	32	P	Control Register A	[CA1/CA2]	<u>IRQ6</u>
10	33	P	Control Register B	[CB1/CB2]	<u>IRQ7</u>
1C	34		Keyboard ASCII		
1D	35		Keyboard Buttons		
1E	36	P	Control Register A	[CA1]	<u>IRQ5</u>
1F	37	P	Control Register B	Bell Control	
20	40				
21	41				
22	42	P	Control Register	60Hz clock	<u>IRQ2</u>
23	43	P	Control Register		<u>IRQ4</u>
24	44		Photoreader Data		
25	45		NPR Control Register		
26	46	P	Control Register A	[CA1]	<u>IRQ3</u>
27	47	P	Control Register B	[CB1/CB2]	<u>IRQ10</u>
28	50		NPR Address	<15:08>	
29	51		NPR Address	<7:0>	
2A	52	P	Control Register A	Tape ←	<u>IRQ11</u>
2B	53	P	Control Register B	Tape →	<u>IRQ12</u>
2C	54		NPR Data to BUS	<15:08>	
2D	55		NPR Data to BUS	<7:0>	
2E	56	P	Control Register	Here Is	<u>IRQ13</u>
2F	57	P	Control Register	Paper	<u>IRQ14</u>

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APP



# I/O Registers

30	60	NPR Data from BUS	<15:08>	
31	61	NPR Data from BUS	<7:6>	
32	62	P Central Register A	Break	IRQ15
33	63	P Central Register B	Repeat	IRQ16
34	64	} Dummy		
35	65			
36	66			
37	67			
70		} Dummy		
71				
72				
73				
74		} Dummy		
75				
76				
77				



# Direct variables

100 -	OPR OPR +1	}	GRAPHIC Input Word
102 -	GCSR		GRAPHIC STATUS Register
103 -	LPCNTA	}	Division counter
104 -	DND		Dividend
106 -	DSA		Divisor
110 -	QT		Quotient
112 -	VCNTA		Vector plot counter
114 -	CHK		in range check word for Plotting
116 -	SADEX		Temporary save location
120 -	BT/LT		
122 -	CADR		
124 -	RDADD		
126 -	RAD		
130 -	HADD		
X 131 -			
132 -	BYTE		
133 -			
134	ROWADD		
136	ROW RST		
140	ROW UPD		
142	COL ADD		
144	COL RST		
146	COL UPD		

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ADD







200-201 CURLOC

202-203 PGMSTK

204-205 CRSTK

206-207 PMSTK

210 SRTLIN

211 LINES

212 LOCK

213 TFLAG

214 RFLAG

215 CURCNT

216 UPDFLAG

217 GRAPH

220 ONLINE

221 NALL

222 NUMB

223 DVAL

224 CURS

225 CNTR

226 DRCTN

227

— Data in Cursor Indicator

230 IBIS

231 IBIC

232 OBIS

233 OBIC

234 DCTL

235 CWORD

236 K1

237 K2

— Write Character Routine (Graphic)



# Direct Variables

240 CLPNTR

242 CLBASE

244 CLP BOT

246 CLPTOP

250 CL BOT

252 CLTOP

254 PLPNTR

256 PLBASE

260 TXTCBS

261 CURDSP

262 NJSR

262 NJSR

264 GJSR

266 PRNT ON

270 CLTEMP

272 EVNSCN

274 ODDSCN

276 OVBASE

Printer Handler

used By Graphic Clear Routine



## Direct variables

300 DSP0

302 DSP1

304 DSP2

306

310 PAGEM

311 PAGEX

312 PAGEY

313 GCNT

314 ESH

315 ESL

316 GTABLE

320 GSTADD

322 GLBASE

324 GLCNT

326 GPCNT

327

330 GRAMB

332 TCLR

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Direct variables

340 - TEMP A

341 - TEMP B

342-343 TEMP X

344 PRX

346 IVX

350 RVEC

352

354 VA

355 VL

356 VAL

360-1 'TO' Address

362-3 'From' Address

364 length

FILL

366

370

372

374

376

off

1

1

2

2

2

2

2

2



other variables

10

1 0 - 1 255<sub>8</sub>

Text & Graphic Control Data

1 300 - 1 377

DSP STK

2 0 - 2 117

STRING

2 120 - 2 147

@STRNO

2 200 - 2 220

Y Table

2 230 - 2 250

X TABLE

2 260 - 2 277

PLOT Table

2 300 -

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ARR



# BASIC Control ROM (Top 2K Bytes)

174000<sub>8</sub>

START	216	14	277	LDS	14	77	set stack pointer	
	275	<u>370</u>	<u>200</u>	JSR	CLKD		start CLOCK	
	275	<u>371</u>	<u>110</u>	JSR	RAMSET		set up character RAM	
	275	<u>371</u>	<u>150</u>	JSR	DSINIT		start Display	
	275	<u>373</u>	<u>200</u>	JSR	BRNSET		set up Break Key	
	275	<u>373</u>	<u>350</u>	JSR	DATSET		set up TERMINAL INPUT	SKIP
	275	<u>374</u>	<u>60</u>	JSR	KBSSET		Set up Keyboard, Bell, Repeat, & Photo Reader	
	275	<u>375</u>	<u>160</u>	JSR	SETSCN		Set up Scrolling Buttons	
	275	<u>376</u>	<u>210</u>	JSR	SYST		Print System ID	
	376	360	0	LDX	360	0	} is next 2K installed	
	214	255	255	CPX	255	255		DONE
	46	5		BNE	SELF		NO - branch	
	376	360	2	LDX	360	2	} else load Transfer vector and Jmp	
	156	0		JMP	0, X			CHAR
SELF	40	376		BR	SELF			

(4354)

DONE



174120<sub>8</sub>

CTLP	227	340	STA A TEMP A	same character
	177	0 341	CLR TEMP B	enable printing of character
	40	7	BR SKIP	
	φ	φ φ		
	φ	φ		
	φ	φ		
SKIP	376	360 0	LDX 360 φ	get first location of next
	214	255 255	CPX # 255 255	lower 2H block is it 255 255
	46	5	BNE NONEX	if not - no transfer
	376	360 4	LDX 360 4	else load transfer vector
	255	φ	JSR φ, X	JSR to transfer vector
NONEX	175	φ 341	TST TEMP B	is printing inhibited
	46	11	BNE DONE	if yes - branch
CHARX	226	34φ	LDA A TEMP A	} get character and mask
	232	230	ORA A IBIS	
	224	231	AND A IBIC	
(4354)	275	<u>374</u> <u>270</u>	JSR CHAR	print character
DONE	71		RTS	Return to caller



# Clock Interrupt Setup

174200<sub>8</sub>

CLKO	17	SEI	hold interrupts		
(14402)	316	<u>370</u>	<u>300</u>	LDX # CLKINT	} setup Vector
	377	17	342	STX IRQ (= \$0FE2)	
	206	5		LDA A, #05	} set clock interrupt control
	227	42		STA A, \$22	
	226	40		LDA A, \$20	clear any pending interrupt
	206	40		LDA # SP	get space character
	316	40	0	LDX 40, 0	first character of Control line
LOOP	247	0		STA A, 0, X	} put spaces in all 80 characters of Control line
	10			INX	
	214	40	120	CPX # 40, 120	
	46	370		BNE LOOP	



316	40	70	LDX # 40 70	address for time display
206	60		LDA A #0	get a zero character
247	0		STA A 0,X	} STORE 0's
247	1		STA A 1,X	
247	3		STA A 3,X	
247	4		STA A 4,X	
247	6		STA A 6,X	
247	7		STA A 7,X	
206	72		LDA A #' :	get : character
247	2		STA A 2,X	} Store ':'s
247	5		STA A 5,X	
206	101		LDA A #A	store A
247	11		STA A 9,X	
206	115		LDA A #' M	store M
247	12		STA A 10,X	time Display now 00:00:00 AM
16			CLI	allow interrupts
71			RTS	Return to Caller

\*\*\* CLOCK NOW ON !!! \*\*\*

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Bob



# ⑤ Clock Interrupt Handler

174300<sub>8</sub>

CLKINT	175	0	214	TST	RFLAG	test for Data inhibited
	47	11		BEQ	TIME	if not - branch
	206	55		LDA	A # \$20	} Restart transfers from PDP 11
	227	32		STA	A # 1A	
	226	30		LDA	A # 18	set printer ready flag
	177	0	214	CLR	RFLAG	data not inhibited
TIME	172	0	223	DEC	DVAL	$\frac{1}{60}$ th of a second
	46	6		BNE	TDONE	1 second? no - branch
	206	74		LDA	A # 60 <sub>10</sub>	60 60ths
	227	223		STA	A DVAL	reset counter
	227	213		STA	A TFLAG	set 1 second Flag
TDONE	226	46		LDA	A # 20	clear interrupt flag
	177	0	222	CLR	NUMB	allow characters during next 60th
	73			RTI		return from interrupt



## ① Time CLOCK ROUTINE (executed during Vertical Retrace)

174340<sub>8</sub>

TIMCLK	175 0 213	TST TFLAG	a second yet?
	47 134	BEQ TIMDON	NO - Done
	177 0 213	CLR TFLAG	Reset T-flag
	316 40 70	LDX #40 70	time display address
	154 7	INC 7, X	increment seconds
	206 71	LDA A #'9	} are we past 9 ?
	241 7	CMP A 7, X	
	54 116	BGE TIMDON	if not - Done
	306 60	LDA B #'0	} restore digit to 0
	347 7	STA B 7, X	
	154 6	INC 6, X	increment 10's of seconds
	206 65	LDA A #'5	} are we past 5 tens ?
	241 6	CMP A 6, X	
	54 102	BGE TIMDON	if not - Done
	347 6	STA B 6, X	restore digit to 0

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154	4	INC	4, X	increment Minutes
206	71	LDA	A #39	} are we past 9?
241	4	CMP	A 4, X	
54	70	BGE	TIMDON	if not - Done
347	4	STAB	4, X	restore minutes
154	3	INC	3, X	increments 10's of Minutes
206	65	LDA	A #5	} are we past 5?
241	3	CMP	A 3, X	
54	56	BGE	TIMDON	if not - Done
347	3	STAB	3, X	restore count
154	1	INC	1, X	increment Hours
206	62	LDA	A #1	} above 10 hours?
241	0	CMP	A 0, X	
47	14	BEQ	ABove	yes - branch
206	71	LDA	A #9	} past 9 Hrs?
241	13	CMP	A 1, X	
54	36	BGE	TIMDON	if not - Done
347	1	STAB	1, X	reset hours
154	0	INC	0, X	set to 10 hrs
40	30	BR	TIMDON	Done

NI

TIM



ABOVE	206	62	LDA A #2	} post 2 hr ?
	241	1	CMP A 1, X	
	54	22	BGE TIMDONE	if not - Done
	347	0	STA B 0, X	reset 10's of hours
	134		INC B	B = 1
	347	1	STA B 1, X	reset Hours count
	206	101	LDA A #A	} are we in AM
	241	11	CMP A 9, X	
	47	3	BEQ NITE	if yes - Branch
	247	11	STA A 9, X	else make AM
	71		RTS	return
NITE	206	120	LDA A #P	} make PM
	247	11	STA A 9, X	
TIMDON	71		RTS	return



②

1745108

# Character RAM Set UP

```

RAMSET      316 17 377    LDX 17 377    Beginning RAM Address -1
            337 374      STX $FC
            316 177 377    LDX 177 377   Beginning Rom Address -1
            337 376      STX $FE

LOOP        336 376      LDX $FE
            10          INX
            337 376      STX $FE
            246 0        LDA A 0, X    get character data

            336 374      LDX $FC
            10          INX
            337 374      STX $FC
            247 0        STA A 0, X    store character data

            214 37 377    CPX 37 377    are we finished?
            46 355        BNE LOOP      No - Loop
            71           RTS          return
  
```

increment ROM Address

increment RAM Address

store character data

are we finished?

No - Loop

return



②

174550<sub>8</sub>

## Initialize Display System

DSINIT	316	14	100	LDX	14	100	get value for CLPBOT
	337	244		STX	CLPBOT		define CLPBOT
	337	240		STX	CLPNTR		} init value
	337	254		STX	PLPNTR		
	206	1		LDA	A	#1	set all lines to 1 character long
	40	1		BR	ENTER1		go start
LOOP1	10			INX			point to next Location
ENTER1	247	φ		STA	A	φ, X	φ, X ← 1
	214	17	331	CPX	17	331	Finished
	46	370		BNE	LOOP1		No-Loop
	337	246		STX	CLPTOP		set top
	316	40	120	LDX	40	120	first Address for characters
	337	250		STX	CLBOT		Define bottom
	337	242		STX	CLBASE		} init value
	337	256		STX	PLBASE		

20120

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RPS



206 52 LDA A 'X' get 'X'  
40 1 BR ENTER2 go to ENTER2

LOOP2 10 INX next location

ENTER2 247 0 STA A 0, X set location = X

214 57 257 CPX 57 - 257 finished

46 370 BNE LOOP2 if not - loop

337 252 STX CLTOP set top of character memory

316 1 0 LDX 1 0 Base for Text

337 260 STX TXTCBS low addr = CURDSP = 0.

117 CLR A set A = 0

227 212 STA A LOCK Display Locked

227 217 STA A GRAPH Graphics off

114 INC A

227 215 STA A CURCNT 1 character

227 216 STA A UPDFLAG do immediate update



## \*\*\* Set UP TEXT STOP LINE \*\*\*

316	1	0	LDX	1	0	} set to turn Display off
377	1	222	STX	1	222	
316	7	26	LDX	7	26	} Restart at control line
377	1	224	STX	1	224	

## \*\*\* Set Up Control START LINE \*\*\*

316	40	0	LDX	40	0	} Scan address
377	1	226	STX	1	226	
316	120	1	LDX	120	1	} 80 character line Display ON
377	1	230	STX	1	230	
316	7	16	LDX	7	16	} next interrupt @ 16
377	1	232	STX	1	232	

## \*\*\* Set UP Control STOP LINE \*\*\*

316	1	0	LDX	1	0	} turn Display 'off'
377	1	236	STX	1	236	
316	7	377	LDX	7	377	} inhibit further interrupts
377	1	240	STX	1	240	



\*\*\* Set Up of Display Stack for TEXT \*\*\*

(5636)	275	<u>372</u>	<u>250</u>	JSR	LDATA	do first line
	206	27		LDA	A #27	} set for 23 iterations
	227	225		STA	A CNTR	
LOOP3	174	0	241	INC	CLANTR+1	
	174	0	243	INC	CLBASE+1	
(5670)	275	<u>372</u>	<u>310</u>	JSR	UPDTXT	get update Text Line
✓	172	0	225	DEC	CNTR	finished?
	46	362		BNE	LWP3	No - Loop

\*\*\* Set ALL Display Control Registers \*\*\*

316	∅	∅	LDX	∅. ∅	
337	2		STX	\$2	} Clear <u>ALL</u> Display Control Registers
337	6		STX	\$6	
337	12		STX	\$A	
337	16		STX	\$E	
316	377	377	LDX	377 377	
337	∅		STX	\$∅	Display Address
337	4		STX	\$4	Cursor Address



316 0 377 LDX 0 377

337 10 STX \$8 Scan counter IN  
Interrupt OUT

316 377 177 LDX 377 177

337 14 STX \$C characters per line  
Display Control

316 4 4 LDX 4 4

337 2 STX \$2

337 6 STX \$6

337 12 STX \$A

337 16 STX \$E

} Set Controls to  
Access Data Registers

206 377 LDA A 377

227 11 STA A \$9

} inhibit scanline interrupts

316 1 0 LDX 1 0

337 14 STX \$C

} Turn off Display

336 242 LDX CLBASE

337 4 STX CURSOR (= \$4)

} set Cursor  
Position



\*\*\* Set Interrupt Vectors \*\*\*

(6060) 316 372 100 LDX # DISINT  
377 17 336 STX # NMI3 = (17 336) } set line scan interrupt

(6074) 316 372 350 LDX # VERTIN  
377 17 334 STX # NMI2 = (17 334) } set vertical retraces interrupt

316 54 4 LDX 54 4 } Setup NMI stroke  
337 16 STX \$E

316 4 7 LDX 4 7

337 12 STX \$A Turn on line scan interrupt

337 6 STX \$6 Turn on Vertical retraces interrupt.

\*\*\* Display is ON !!! \*\*\*

38 71 RTS return to Caller



②

19

1751008

## \*\*\* Display Interrupt Handler \*\*\*

DISINT	336	300	LDX	DSP0	} Load New Scan Address
	337	0	STX	\$0	
	336	302	LDX	DSP1	} Load # of Characters & New Display Control
	337	14	STX	\$C	
	336	304	LDX	DSP2	} Load New Line Count & New Interrupt Line #
	337	10	STX	\$8	

## \*\*\* Display Set for next line!!! \*\*\*

(6230)	275	<u>372</u>	<u>130</u>	JSR	DSPUPD	go setup for next interrupt
	226	11		LDA	A \$9	clear interrupt (dummy load)
	226	14		LDA	A \$C	NM2 strobe (dummy load)
	73			RTI		returns from interrupt

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APD



16

175130<sub>8</sub>

\*\*\* Display Update Routine \*\*\*

DISUPD

237 202

STS PGMSTH

same stack pointer

336 204

LDX CRSTK

get current Display  
Stack position

356 0

LDX 0,X

get address of next  
line Display Control  
information

256 0

LDS 0,X

237 300

STS DSP0

256 2

LDS 2,X

237 302

STS DSP1

256 4

LDS 4,X

237 304

STS DSP2

Update Display  
data

336 204

LDX CRSTK

10

INX

10

INX

337 204

STX CRSTK

update stack  
position

236 202

LDS PGMSTH

restore stack pointer

71

RTS

return

(6360)  
Ro  
(0366)



⑤ \* \* \* SCROLL UP ROUTINES \* \* \*

175170g

(6360)	ROTUP	275	<u>372</u>	<u>200</u>	JSR	CURUP	update pointer to
(6366)		275	<u>372</u>	<u>250</u>	JSR	LD DATA	Control data
		71			RTS		return to caller

④

175200g

	CURUP	226	261		LDA	A	CURDSP	
		201	212		CMP	A	#212	are we at top of area?
		45	3		BCS	C1		no - branch
		117			CLR	A		else set at bottom
		40	2		BR	C2		branch ahead
	C1	213	6		ADD	A	#6	increment position
	C2	227	261		STA	A	CURDSP	restore CurDsp
		71			RTS			return to caller



\*\*\* Scroll Down routine \*\*\*

①

175 220<sub>8</sub>

(6440) ROTDWN

275 372 250

JSR LD DATA

(6446)

275 372 230

JSR CURDWN

71

RTS

②

175 230<sub>8</sub>

CURDWN

226 261

LDA A CURDSPA get current Display

46 4

BNE C3

position  
not at bottom - Branch

206 212

LDA A # 212 char set at top

40 2

BR C4

branch ahead

C3

200 6

SUB A # 6

Decrement Position

C4

227 261

STA A CURDSPA

restore CURDSP

71

RTS

return to Caller



③ \*\*\* Set UP Text Control Data \*\*\*

175 250<sub>g</sub>

LDDATA	336 254	LDX PLPNTR	get address of current line character count
	346 0	LDA B 0, X	get character count
	336 260	LDX TXTCBS	get Address of Text Control Location
	347 2	STA B 2, X	set character count
	306 1	LDA B #1	} set Display Control
	347 3	STA B 3, X	
	306 17	LDA B #17	} set next scan line interrupt
	347 5	STA B 5, X	
	306 7	LDA B #7	} set new line #
	347 4	STA B 4, X	
	326 257	LDA B PLBASE+1	} Low order Base Address
	347 1	STA B 1, X	
	326 256	LDA B PLBASE	} High order Base Address
	347 0	STA B 0, X	
	71	RTS	return to Caller

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ASD



\*\*\* Text Update Subroutine \*\*\*

②

175310g

UPDTXT 175 0 212  
47 1  
71

TST LOCK  
BEQ D1  
RTS

is display locked  
to incoming data  
yes - branch  
else return

D1 336 254  
246 0  
336 260  
247 2  
336 246  
234 254  
46 1  
71

LDX PLPNTN  
LDA A 0, X  
LDX TXTCBS  
STA A 2, X  
LDX CLPNTN  
CPX PLPNTN  
BNE D2  
RTS

} get current character  
count

} store current count  
@ current Text control loc.

} are we still in same  
line of text.

no - branch

yes - return

D2 337 254  
336 242  
337 256  
(6706) 275 372 170  
71

STX PLPNTN  
LDX CLBASE  
STX PLBASE  
JSR ROTUP  
RTS

update to next line of text

} update Data Base pointer

go set up next text line  
return

VERT

NOGRA

E1



# \*\*\* Vertical Retrace Interrupt Handler \*\*\* 23

①

175350<sub>8</sub>

VERTIN	175	0	216	TST	UPDFLAG	is update required?
	47	132		BEQ	RESTART	no - just go restart
	177	0	216	CLR	UPDFLAG	doing update
	237	202		STS	PGMSTK	same stack pointer
	316	1	370	LDX	#DSPSTH+56.	Display stack address for last text line
	216	1	234	LDS	#CLSTP	control text line stop control address put on stack
	257	6		STS	6, X	
	216	1	226	LDS	#CLSRT	control text line start control address put on stack
	257	4		STS	4, X	
	216	1	220	LDS	#TXTSTP	Text Stop Control
	257	2		STS	2, X	put on stack
	175	0	217	TST	GRAPH	graphics ON?
	47	4		BEQ	NOGRAPH	no - branch
	206	11		LDA	A #9,	9 lines with graphic
	40	2		BR	E1	
NOGRAPH	206	26		LDA	A #22	22 lines without graphic
E1	227	211		STA	A LINES	lines of text!

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226	260	LDA A	TXTCBS	} Text Control Region Address of last line of Text
326	261	LDA B	CURDSP	
40	2	BR	EZ	branch to entry

LOOP	11	DEX	} point to preceding stack position
	11	DEX	

EZ/ ⊕	247	0	STA A	0, X	put High order Address on Stack
	347	1	STA B	1, X	put Low order Address on Stack
	300	6	SUB B	#6	let CURDSP point to preceding Text line
	44	2	BCC	CONT	have we passed bottom of stack? No - cont
	306	212	LDA B	#212	else load Top of Stack area
CONT	172	0	DEC	LINES	are we finished yet?
	46	357	BNE	LOOP	No - Loop

\*\*\* Text portion of Display stack is now Loaded \*\*\*

175	0	217	TST	GRAPH	is graphic on?
46	4		BNE	CONT1	yes - branch
206	57		LDA A	#STLT	else only Text scan line for starting display
40	20		BR	CONT2	branch

\*\*\* Set up graphics on Display stack \*\*\*

CON  
CONT2  
RESTA  
EVEN  
ODD



CONT 1	11		DEX	
	11		DEX	} put Address of GRAPH Stop on Display stack
	216	1 250	LDS # GRPSTP	
	257	0	STS 0, X	
	11		DEX	} put Address of Graph Start on Display stack
	11		DEX	
	216	1 242	LDS # GRPSTP	
	257	0	STS 0, X	
	206	17	LDAA # SRTLG	starting line in graphic
CONT 2	227	210	STA A SRTLIN	store at start line
	337	206	STX PMSTH	save beginning of Display Stack
	236	202	LDS PGMSTH	restore stack

\*\*\* Restart Display for Current Scan \*\*\*

RESTART	175	0 217	TST GRAPH	is graphics on ?
	47	16	BEQ CONT 3	no - branch
	175	0 15	TST \$D	is this an ODD frame
	52	4	BPL EVEN	even - branch
	336	274	LDX ODDSCN	oddscore Address
	40	2	BR ODD	
EVEN	336	272	LDX EVNSCN	evenscan address
ODD	377	1 242	STX GRPSTP	set up graph start

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ADD



CONT3	336	206	LDX	PmSTM	} reset current DSP STACK
	337	204	STX	CRSTK	

(7274) 275 372 130 JSR DSP UPD } go set up Display Data for first Text interrupt

226 210 LDA A SRTLIN } set interrupt for starting

227 11 STA A \$9

226 5 LDA A \$5 clear interrupt (dummy load)

226 14 LDA A \$C NMI strobe (dummy load)

(7322) 275 370 346 JSR TIMCLK } Go Do Time update

336 200 LDX CURLOC } set cursor position only during refresh

337 4 STX CURSOR

\* \* \* PATCHES FOR Code to Executed At Address \* \* \*

377 377 376

377 377 376

377 377 376

377 377 376

73

RTI

Return from Interrupt



⑫ \*\*\* BREAK KEY SETUP - LINE/LOCAL CONTROL \*\*\* 25

175600g

BRKSET	17	SEI	hold interrupts
(7002)	316 373 230	LDX #BRKINT	} setup Break Key interrupt Vector
	377 17 374	STX IRQIS (\$FFC)	
	206 7	LDA A #7	} turns on interrupt
	227 62	STA A \$32	
	226 60	LDA A \$30	clear pending interrupt
	177 0 220	CLR ONLINE	make 'Local'
	16	CLI	allow interrupts
	71	RTS	Return to caller

⑬ \*\*\* BREAK KEY Interrupt Handler \*\*\*

175630g

BRKINT	316 40 10	LDX 40 10	} address for control word "T0"
	337 360	STX \$F0	
	206 7	LDA A #7	} # of characters to transfer
	227 364	STA A \$F4	

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AM



163 0 220

COM ONLINE

47 7

BEQ X1 Now LOCAL - branch

(7514)

316 373 300

LDX #TXT1 'ON LINE'

227 214

STA A RFLAG indicate Data was inhibited

40 12

BR X2

(7532)

X2

316 373 310

LDX #TXT2 '-LOCAL-'

206 5

LDA A #5

227 32

STA A \$1A

} inhibit ready flag

to POP-11

177 0 214

CLR RFLAG

don't allow clock to  
re enable flag

X2

337 362

STX \$P2

From Address

(7562)

275 373 320

JSR FILL

go transfer characters

226 60

LDA A #30

clear interrupt flag

#

73

RTI

return from interrupt

175

175

175

FILL



①

175700g

117	116	40	TXT1	'ON_LINE'
114	111	116	105	

②

175710g

40	114	117	TXT2	'_LOCAL_'
103	101	114	40	

③

175720g

FILL	336	362	LDX #F2	'From' Address
	246	0	LDA A 0,X	get character
	10		INX	} update 'From' Address
	337	362	STX #F2	
	336	360	LDX #F0	'To' Address
	247	0	STA A 0,X	store character
	10		INX	} update 'To' Address
	337	360	STX #F0	
	172	0 364	DEC #F4	finished?
	46	355	BNE FILL	no - go to FILL
	7 1		RTS	return to Caller

16 July 78  
AM



(4)

\*\*\* UNIBUS DATAIN HANDLER Setup \*\*\*

175750<sub>8</sub>

DATSET	17	SET	hold interrupts
(7722)	316 <u>374</u> <u>10</u>	LDX #	TERMDI } set up interrupt
	377 17 352	STX	<u>IRQ6</u> = (\$FEA) } Vector
	177 0 32	CLR \$1A	clear Control Register
	177 0 30	CLR \$18	set for input
	206 55	LDA A #20	set to access data, interrupt when data written into Buffer
	227 32	STA A \$1A	set control
	226 30	LDA A \$18	clear pending interrupt (dummy load)
	206 3	LDA A #3	} set for 180 char/sec
	227 221	STA A NULL	
	16	CLI	enable interrupts
	71	RTS	return to caller

(10034)

CA  
TEND



① \*\*\* TERMINAL INTERRUPT HANDLER (INPUT) \*\*\* 27

176010g

TERMDI	206	5	LDA A #5	} inhibit printer ready flag
	227	32	STA A \$1A	
	226	30	LDA A \$18	get character in A reg
(1034)	275	<u>370</u> <u>120</u>	JSR CTLP	go process character
	174	0 222	INC NUMB	increment counter for # of characters this 60th
	117		CLR A	
	175	0 220	TST ONLINE	are we online?
	47	17	BEQ TEND	No - branch
	226	221	LDA A NALL	} are we at limit of characters per 60th
	221	222	CMP A NUMB	
	57	7	BLE CA	yes - branch
	206	55	LDA A # \$20	} else set control to strobe printer flag & allow more characters
	227	32	STA A \$1A	
	226	30	LDA A \$18	set ready flag
	73		RTI	return from interrupt
CA	206	377	LDA # \$FF	printer inhibited
TEND	227	214	STA A RFLAG	set flag for clock
	73		RTI	return from interrupt

16 July 78  
ABD



④ \*\*\* KRB P Control Set Up \*\*\*

176 0608

KBSET	17	SEI	hold interrupts
(10142)	316 <u>374</u> <u>234</u>	LDX #RPINT	} Repeat Button interrupt vector
	377 17 376	STX <u>IRQ16</u> (= \$FFE)	
(10156)	316 <u>374</u> <u>236</u>	LDX #KBINT	} Keyboard interrupt vector
	377 17 350	STX <u>IRQ5</u> (= \$FE8)	
(10172)	316 <u>374</u> <u>230</u>	LDX #PHDR	} photoreader interrupt vector
	377 17 344	STX <u>IRQ3</u> (= \$FE4)	

\*\*\* Set Up Keyboard \*\*\*

177 0 36	CLR \$1E	clear Keyboard Control
177 0 34	CLR \$1C	Set for input
206 7	LDA A #7	} set to interrupt on every character
227 36	STA A \$1E	
226 34	LDA A \$1C	clear pending interrupt

\*\*\* Set Up Bell \*\*\*

177 0 37	CLR \$1F	clear switch control
177 0 35	CLR \$1D	Set to Read Switches
206 54	LDA \$2C	} set to strobe Bell on dummy write to \$1D
227 37	STA A \$1F	



\*\*\* Set Up Repeat Button \*\*\*

206	370	LDA A # \$F8	} mask bits 0,1,2
224	63	AND A \$33	
212	5	ORA A #5	OR in Repeat Control
227	63	STA A \$33	Set Control Register
226	61	LDA A \$31	clear any pending interrupt

\*\*\* Set Up BUS Output Control \*\*\*

177	0	33	CLR \$1B	Clear BUSO Control Register
206	377	LDA A # 377	} set direction for output	
227	31	STA A \$19		
206	54	LDA A # \$2C	} Ready Flag strobe-control	
227	33	STA A \$1B		

\*\*\* Photo Reader Setup \*\*\*

177	0	46	CLR \$26	clear photoreader control
177	0	44	CLR \$24	set direction in
206	7	LDA A #7	} set interrupt control	
227	46	STA A \$26		
226	44	LDA A \$24	clear pending interrupt	

16 Jul 78  
ABD



xxx

# MASKING VARIABLES

xxx

17-0000p

206 0 LDA A # 0 } OR'd with incoming data  
 227 230 STA A IBIS

206 177 LDA A # 177 } And'd with incoming data  
 227 231 STA A IBIC

206 200 LDA A # 200 } OR'd with outgoing data  
 227 232 STA A OBIS

206 377 LDA A # 377 } And'd with outgoing data  
 227 233 STA A OBIC

177 0 234 CLR DCTL allow Control Character printing

16 CLI enable interrupts  
 7E RTS return to Caller

Ⓟ

17

PHI

RPI

KBI

IN

(10512)

Dur



9

\*\*\* KRP interrupt Handler \*\*\*

1762308

PHRDR	226	44	LDA A \$24	get character from reader auto clear interrupt flag
	40	4	BR IN	

RPINT	226	61	LDA A \$31	clear repeat interrupt flag
-------	-----	----	------------	-----------------------------

KBINT	226	34	LDA A \$1C	get keyboard Data clear interrupt flag
-------	-----	----	------------	---

IN	175	0	226	TST ONLINE
	46	4		BNE DUMP

(10512)	275	<u>370</u>	<u>120</u>	JSR CTLP	
	73			RTI	return from interrupt

DUMP	232	232	ORA A OBIS	or to set a bit
	224	233	AND A OBIC	And to clear a bit
	227	31	STA A \$19	send character to PDP-11 auto set ready flag
	73		RTI	return from interrupt

16 Jul, 78  
/000



\*\*\*

## CHAR ROUTINE

\*\*\*

176270<sub>8</sub>

(See patch 19 Sept 78 pg 39)

30

DC

66

CHAR	201	15	CMP A #'(CR)	is character a CR ?	
	46	11	BNE DA	if not - branch	
	206	1	LDA A #1	} get a count of 1 as position	37
	227	215	STA A CURCNT		
	336	242	LDX CLBASE	} position Cursor	
	337	200	STX CURLOC		
	71		RTS	return to caller	

DD

DA	201	12	CMP A #'(LF)	is character a LF ?		
	46	13	BNE DB	if not - branch		
7 (0622)	275	<u>375</u>	<u>100</u>	JSR UPDLIN	reposition line	40
	226	215	LDA A CURCNT	get current count		
	336	240	LDX CLPNTR	get CLPNTR Address		
	247	0	STA A 0,X	set line length		
	40	<u>75</u>	BR DX	go finish!	55	

DB	201	7	CMP A #'(B <sub>1</sub> )	is character a B <sub>1</sub> ?	
	46	2	BNE DC	if not - branch	
	227	35	STA A \$1D	ring bell!!!	

70

107



30

DC

175  $\emptyset$  234

TST DCTL

print controls?

47 5

BEQ DD

yes - Branch

221 234

CMP A DCTL

do we print character?

37

44 1

BCC DD

Yes - Branch

71

RTS

if no - return/finished

DD

336 200

LDX CURLOC

load address of Text Cursor

247  $\emptyset$ STA A  $\emptyset$ , X

put new character in position

16

INX

} update cursor position

 $\emptyset$ 

337 200

STX CURLOC

306 40

LDA B<sup>u</sup> (F<sub>p</sub>)

} place a sp at the cursor position

347  $\emptyset$ STA B  $\emptyset$ , X

SS

174 0 215

INC CURCNT

update current count

226 215

LDA CURCNT

load count

336 240

LDX CLPNTR

get address of line length

241  $\emptyset$ CMP A  $\emptyset$ , X

is current &gt; (CLPNTR)

55 2

BLT DE

no - branch

247  $\emptyset$ STA A  $\emptyset$ , X

else update CLPNTR

16 Jun, 78  
AOD



1764  
PDSO

DE	206	121	LDA A # 81.	} are we past 80 characters?
	241	0	CMP A 0, X	
	56	262	BGT PATCH	if not go finish
	227	35	STA A \$10	else ring bell!!!
	247	0	STA A 0, X	and set count = 81.

(11014)	275	<u>375</u>	<u>100</u>	JSR UPDLIN	reposition line
	336	240		LDX CLPNTR	get address of new line character count
	206	11		LDA A # 11.	} set line length = 11.
	247	0		STA A 0, X	
	227	215		STA CURCNT	

(11042)	DX	275	<u>375</u>	<u>60</u>	JSR CLEAR	flush new line
(11050)	DX	275	<u>375</u>	<u>40</u>	JSR POSCUR	reposition cursor
(11056)		275	<u>372</u>	<u>310</u>	JSR UPDXT	update text
	DX	174	0	216	INC UPDFLAG	update screen during next retrace
		71			RTS	return to caller

10550	PATCH	275	372	310	JSR UPDXT	update text - no screen update
		71			RTS	

1764  
CLEA  
BA



\*\*\* POSITION CURSOR SUBROUTINE \*\*\*

①  
176440<sub>8</sub>

POSCUR	336 240	LDX CLPNTR	get address of current line length
	246 0	LDA A 0,X	get # of characters
	112	DEC A	delete CURSOR position
	137	CLR B	} compute new position
	233 243	ADD A CLBASE+1	
	331 242	ADC B CLBASE	
	227 201	STA A CURLOCS	} set Cursor at position
	327 200	STA B CURLOC	
	71	RTS	return to caller

\*\*\* Clear Characters Subroutine \*\*\*

①  
176460<sub>8</sub>

CLEAR	336 240	LDX CLPNTR	} get character count at current line
	246 0	LDA A 0,X	
	336 242	LDX CLBASE	get address of first character in line
	306 40	LDA B #SP	get a SP character
BA	347 0	STA B 0,X	'clear' first location
	10	INX	next position
	112	DEC A	more characters?
	46 372	BNE BA	yes - branch
	71	RTS	else return to Caller

16 Jul 78  
AGS



① \*\*\* UPDATE LINE POINTERS Subroutine \*\*\*  
 176 500

UPDLIN	336	240	LDX	CLPNTR	} get # of characters in current line
	246	0	LDA	A, X	
	201	1	CMP	A # 1	is count = 1
	47	3	BEQ	AA	yes - branch always need 1 character
	152	0	DEC	0, X	} else delete cursor space!
	112		DEC	A	
AA	234	246	CPX	CLPTOP	are we at top?
	47	3	BEQ	AB	if yes - branch
	10		INX		else increment pointer
	40	2	BR	AC	
AB	336	244	LDX	CLPBOT	set to bottom
AC	337	240	STX	CLPNTR	update counter stack

AD



\*\*\* Check for character storage limit \*\*\*

336	242	LDX	CLBASE	Present base in X Reg
137		CLR	B	} compute new CLBASE
233	243	ADD	A CLBASE+1	
331	242	ADC	B CLBASE	
227	243	STA	A CLBASE+1	} update CLBASE
327	242	STA	B CLBASE	
220	253	SUB	A CLTOP+1	} are we past top of character Area?
322	252	SBC	B CLTOP	
45	6	BCS	AD	if not-branch & return
337	276	STX	OVBASE	else note last base before going over top !!
336	250	LDX	CLBOT	} Reset CLBASE to Bottom
337	242	STX	CLBASE	
AD	71	RTS		Return to Caller

16 July 78  
ADJ



② \*\*\*

# SCAN BUTTON Setup \*\*\*

176560<sub>8</sub>

SETSCN 17

SEI

hold interrupts

(11342)

316 375 220

LDX #SCLUP

} Scrollup interrupt  
vector

377 17 366

STX IRQIL (= \$FF6)

(11356)

316 375 370

LDX #SCLDN

} Scroll Down interrupt  
vector

377 17 364

STX IRQII (= \$FF4)

206 5

LDA A #5

227 52

STA A \$2A

227 53

STA A \$2B

} enable interrupts

226 54

LDA A \$28

226 51

LDA A \$29

} clear pending interrupts

16

CLI

enable interrupts

71

RTS

Return to Caller

②

1766

SCRL

(11504)

(11512)

(11520)

BOTH

11536

SAM

LEXI



①

\*\*\*

TAPE →

Interrupt Handler

\*\*\*

33

176 620<sub>g</sub>

SCRLUP	226	51	LDA \$29	clear interrupt flag	
	226	35	LDA #1D	read keyboard switches	
	205	40	BIT A #40	is Tape depressed	
	46	25	BNE BOTH	if yes - branch	
	336	254	LDX PLPNTR	} PLPNTR = CLPNTR ?	
	234	240	CPX CLPNTR		
	47	21	BEQ SAME	if yes - branch	
	206	377	LDA A #377	} unlock Display	
	227	212	STA A LOCK		
(11504)	275	<u>375</u>	<u>270</u>	JSR UPSCRL	go set up for up scrolling
(11512)	275	<u>375</u>	<u>320</u>	JSR RLPUP	roll pointer up 1 line
(11520)	275	<u>372</u>	<u>170</u>	JSR ROTUP	rotate Display 1 line
	40	5	BR	EXIT	
BOTH	226	50	LDA \$28	clear interrupt flag	
11536 SAME	275	<u>376</u>	<u>140</u>	JSR LCHDSP	go Lock Display
EXIT	174	0	216	INC UPDFLAG	update Display during next retroce
	73			RTI	Return from Interrupt

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ARR



② \*\*\* UP Scroll Setup \*\*\*

176670<sub>8</sub>

UPSCRL	175	0 226	TST DRCTN	↑ = UP ↓ = DOWN	
	52	15	BPL UP DONE	if up - we're done	
	177	0 226	CLR DRCTN	else make (UP)	
	206	27	LDA A #23	set counter = 23	
LOOPY	66		PSH A	save counter	
(11606)	275	<u>375</u> <u>320</u>	JSR RLPUP	go roll pointers up 1 line	
	62		PUL A	get counter	
	112		DEC A	are we done?	
	58	370	BGT LOOPY	no - Loop	
UP DONE	71		RTS	Return to Caller	

BX1

BX2

③ \*\*\* ROLL Pointers UP 1 line Subroutine \*\*\*

176720<sub>8</sub>

RLPUP	336	254	LDX CLPNTR	} get character count in current line	
	246	0	LDA A 0, X		
	234	246	CPX CLPTOP	are we at Top	
	46	4	BNE BX1	if not - branch	
	336	244	LDX CLPBOT	else get bottom Location	
	46	1	BR BX2	branch	

BX3



BXI	10	INX	point to next line
BX2	337 254	STX PLPTR	update pointer
	137	CLR B	current count = (B, A)
	233 257	ADD A PLBASE + 1	} compute and same new BASE
	331 256	ADC B PLBASE	
	227 257	STA A PLBASE + 1	
	327 256	STA B PLBASE	
	226 253	LDA CLTOP + 1	} compare is PLBASE ≤ CLTOP
	326 252	LD B CLTOP	
	220 257	SUB A PLBASE + 1	
	322 256	SBC B PLBASE	
	44 4	BCC BX3	yes - branch
	336 250	LDX CLBOT	} clear out PLBASE
	337 256	STX PLBASE	
BX3	71	RTS	Return to Caller

16 July 78  
ARD



\*\*\*  
①

TAPE ← Interrupt Handler

\*\*\*

176 770<sub>8</sub>

SCRLDN	226	50	LDA A \$28	clear interrupt flag	DNSCR
	226	35	LDA A \$1D	read keyboard switches	
	205	20	BIT A #20	is Tape → depressed	
	46	17	BNE BOTH	if yes - Branch	
	206	377	LDA A #377	} unlock display	
	227	212	STA A LOCK		
(12010)	275	<u>376</u>	<u>30</u>	JSR DNSCR	go setup for down scrolling
(12016)	275	<u>376</u>	<u>60</u>	JSR RLPDN	roll pointers down 1 line
(12024)	275	<u>372</u>	<u>220</u>	JSR ROTDWN	rotate display down 1 line
	40	5	BR EXIT	branch	
BOTH	226	51	LDA A \$29	clear interrupt Flag	
(12042)	275	<u>376</u>	<u>140</u>	JSR LCK DSP	go Lock display
EXIT	174	0	216	INC UPDFLAG	update display lines next retrace
	73			RTI	Return from Interrupt

LODPX

(12110)

DND5

177

RLP



\*\*\* DOON SCROLL Setup \*\*\*

1770308

DNSCR	175	0	226	TST	DRCN	+ = up - = Down
	53	16		BMI	DNDONE	if -- we're done
	206	377		LDA	A #377	} set direction Down
	227	226		STA	A DRCN	
	206	27		LDA	A #23.	set counter at 23
LOOPX	66			PSH	A	save counter
(12110)	275	<u>376</u>	<u>66</u>	JSR	RLPDN	Roll pointer 1 line Down
	62			PUL	A	get counter
	112			DEC	A	are we finished
	56	370		BGT	LOOPX	no - Loop
DNDONE	71			RTS		Return to Caller

\*\*\* ROLL Pointers Down 1 line Subroutine \*\*\*

1770608

RLPDN	336	254		LDX	PLPTR	} is pointer at bottom?
	234	244		CPX	CLPBOT	
	46	4		BNE	Ax1	no - branch
	336	246		LDX	CLPTOP	get top
	40	1		BR	Ax2	branch

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AM



AX4	11	DEX	point to previous line	17	
AX2	337 254	STX	PLPNTR	update PLPNTR	LEKDS
	226 251	LDA	A	CLBOT+1	} CLBOT - PLBASE is PLBASE > CLBOT
	326 250	LDA	B	CLBOT	
	220 257	SUB	A	PLBASE+1	
	322 256	SBC	B	PLBASE	
	45 5	BCS	AX3	yes - branch	
	336 276	LDX	OVBASE	} else reset PLBASE to Highest Base Lost Calculated	(12334
	337 256	STX	PLBASE		
	71	RTS		Return to Caller	

AX3	226 257	LDA	A	PLBASE+1	} compute previous Base Address	200f
	326 256	LDA	B	PLBASE		(12356)
	336 254	LDX	PLPNTR			(12364)
	240 0	SUB	A	0, X		
	302 0	SBC	B	0		
	227 257	STA	A	PLBASE+1	} update PLBASE	
	327 256	STA	B	PLBASE		
	71	RTS		Return to Caller		12402



⑤ \*\*\*

## LOCK DISPLAY SUBROUTINE

\*\*\*

36

1771408

LCKDSP	177 0 212	CLR LOCK	set for locked Display
	336 240	LDX CLPNTR	} Set for current pointer
	337 254	STX PLPNTR	
	336 242	LDX CLBASE	} Set for current Base
	337 256	STX PLBASE	

	177 0 226	CLR DIRCTN	Set for scroll up
(12334)	275 <u>376</u> <u>30</u>	JSR DMSCLR	Down scroll to 23rd previous line
	177 0 226	CLR DRCTN	set for Scroll up
	206 27	LDA A #23.	setup for 23 updates

LOOPZ	66	PSHA	save counter
(12356)	275 <u>372</u> <u>170</u>	JSR ROTUP	rotate Display 1 line
(12364)	275 <u>375</u> <u>320</u>	JSR RLPUP	roll pointers 1 line
	62	PUL A	get counter
	112	DEC A	are we finished
	56 365	BGT LOOPZ	no - loop

12402	275 <u>372</u> <u>170</u>	JSR ROTUP	update Lost line
	71	RTS	Return to Caller

③

177 210

16 July 78  
AMD



\* \* \*

# SYSTEM ID

\* \* \*

177 210g

(12420)  
SYST

316 376 240

LDX #SYSTD

point to character string

PRIN

(12426)

275 376 220

JSR PRINT

go print string

72

RTS

(return)

(12454)

DOM

177

SYS

177



\*\*\*

PRINT Subroutine \*\*\*

177 220<sub>8</sub>

PRINT	337	342	STX	TEMPX	same pointer	
	246	0	LDA	0,X	get character	
	47	10	BEQ	DONE	if = 0 Done	
(12454)	275	<u>374</u>	<u>270</u>	JSR	CHAR	go process character
	336	342	LDX	TEMPX	} update pointer	
	10		INX			
	40	362	BR	PRINT	go do another	
DONE	71		RTS		return to caller	

\*\*\*

ID Character Strings \*\*\*

177 240<sub>8</sub>

SYSTD /<sup>c</sup>R /<sup>f</sup>P <sup>s</sup>p 16 <sup>s</sup>p JULY <sup>s</sup>p 1978 <sup>s</sup>p - <sup>s</sup>p TEXT <sup>s</sup>p  
 DISPLAY <sup>s</sup>p ROM <sup>c</sup>R /<sup>f</sup>P \* M<sub>0</sub>/

15	12	40	61	66	40	112	125	114	131	
40	61	71	67	70	40	55	40	124	105	
130	124	40	104	111	123	120	114	101	131	40
122	177	115	15	12	52	0				

177 310<sub>8</sub>



\*\*\*

## Reset Subroutine

\*\*\*

1773208

BEGIN	177	Ø	36	CLR \$1E	Clear KYBD Control Reg.
	177	Ø	34	CLR \$1C	Set Direction for Input
	206	6		LDA A #6	load 6
	227	36		STA A \$1E	Set Access for data
	177	Ø	37	CLR \$1F	Clear Switch Control Register
	177	Ø	35	CLR \$1D	Set for Input
	206	54		LDA A # \$2C	set Bell Control
	227	37		STA A \$1F	
TEST	175	Ø	36	TST \$1E	character typed?
	52	373		BPL TEST	No - Test again
	226	34		LDA A \$1C	get character
	201	114		CMP A #'L	is it an 'L'
	47	<u>23</u>		BEQ LOAD	if yes - go load memory
	201	107		CMP A #'G	is it an 'G'
	47	<u>11</u>		BEQ GO	if yes - go start Rom
	201	123		CMP A #'S	is it an 'S'
	47	<u>10</u>		BEQ START	if yes - go start in RAM
BELLRING	177	Ø	35	CLR \$1D	else ring 'Bell'
	40	350		BR TEST	

⑤

7

17

30

37

47

277



	GO	176	370	0	JMP	370	0	start prog Rom
7	START	176	4	0	JMP	4	0	start Program in RAM
	***	Set UP NPR Logic			***			
	LOAD	177	0	47	CLR	\$27		clear NPR Control Reg
		206	37		LDA	A	# \$1F	} set Direction of Control
		227	45		STA	A	\$25	
		206	54		LDA	A	# \$2C	} Set control for CBZ stroke on write
		227	47		STA	A	\$27	
		216	0	0	LDS	# \$0		put zero in stack
30		316	377	377	LDX	# \$FFFF		all ones in X
		237	52		STS	\$2A		clear NPR Address Cont.
		337	50		STX	\$28		set for output
37		237	56		STS	\$2E		clear Data Out Cont.
		337	54		STX	\$2C		set for output
		237	62		STS	\$32		clear Data In Cont.
		237	60		STS	\$30		set for Input
47		316	4	4	LDX	# \$0404		Set up for Data Access
		337	52		STX	\$2A	} set control registers	
		337	56		STX	\$2E		
		337	62		STX	\$32		

19 Jul 78  
ARD



57	216	1	376	LDS	## 1FE	PDP-11 Address	
	177	0	45	CLR	\$25	NPR central Reg	
	306	1		LDA	B #1	get a 1	IRG
67	316	0	377	LDX	# \$FF	internal Address	SWI
LOOP	10			INX		increment index address	NM
	214	20	0	CPX	# \$1000	at end of 4K?	RES
	47	<u>276</u>		BEQ	BELLRING	going bell if yes	
	61			INX		} increment PDP11 word Address	
	61			INX			
	237	50		STS	\$28	update NPR address Reg	Patch
	327	45		STA	B \$25	initiate NPR	176 27
AGAIN	175	0	45	TST	\$25	finished?	CHA
	52	373		BPL	AGAIN	if not - Test again	
	226	61		LDA	A \$31	get low Byte	177 520 PCHR:
	247	0		STA	A 0,X	store in RAM	
	40	<u>351</u>		BR	LOOP	do another	177 535 PRTRM



RESTART Vectors

IRQ (177770)	376 320	" BEGIN
SWI 177772	376 320	" BEGIN
NMI (177774)	376 320	" BEGIN
RESTART (177776)	376 320	" BEGIN

Patch - 19 Sept 78 ADD

176 270<sub>8</sub>

CHAR:	176 377 120	JMA PCHR:	go to char patch
	1	NOP	

177520<sub>8</sub>  
PCHR:

376 350 0	LDX 350 0	} check for Rom 2
214 255 255	CPX #2 55 255	
46 5	BNE PRTRN	if not there - branch
376 350 10	LDX 350 10	} else jump to Rom 2
156 0	JMP 0, X	

177535  
PRTRN

201 15	CMPA #'C <sub>R</sub>	test for C <sub>R</sub>
46 3	BNE \$Z	
176 374 274	JMP # 176274	

\$Z

176 374 305	JMP # 176305
-------------	--------------

19 July 78  
ADD



\*\*\* Rom 1 - LOCAL MONITOR \*\*\*

(4000)  
170 000<sub>8</sub>

255	255	* 255	255	'Here' indicator
<u>360</u>	<u>10</u>	* INIT		init Vector
<u>360</u>	<u>40</u>	* DATA		data Vector

(4100)  
17  
DATA

(4020)  
170 010<sub>8</sub>

INIT	275	<u>361</u>	<u>326</u>	* JSR	SYSTB	Print RomID
------	-----	------------	------------	-------	-------	-------------

376	350	0	LDX	350	0	} is next Rom There?
214	255	255	CPX	"255	255	
46	5		BNE	NONEX		No - check again
376	350	2	LDX	350	2	} go to transfer Vector
156	0		JMP	0,X		
NONEX	40	376	BR	NONEX		

SK

OU



(4100

176040

DATA

175 0 220

TST ONLINE

online?

46 4

BNE SKIP

if yes - skip

336 262

LDX NJSR

} JSR to designated  
Subroutine

255 0

JSR 0,X

SKIP

376 350 0

LDX 350 0

} is Next Rom there?

214 255 255

CPX \*255 255

46 5

BNE OUT

if not - done

376 350 4

LDX 350 4

} else load transfer  
Vector & Jump

156 0

JMP 0,X

OUT

71

RTS

29 July 78  
ADD



\*\*\* CSRCH - Character Search Routine \*\*\*

170 400g  
(3000)  
CSRCH

326	237	LDA B K2	} two character shifting buffer
327	236	STAB K1	
227	237	STA A K2	
336	236	LDX K1	} search for control sequence @ =
214	100 75	CPX # 3 @ =	
47	1	BEQ FND	if found - branch
71		RTS	else return

FND

275	<u>361</u>	<u>130</u>	JSR GETSTR	get a character string
275	<u>361</u>	<u>60</u>	JSR CHECK	check end of string
316	<u>367</u>	<u>240</u>	LDX #TABLE	table of monitor commands
275	<u>361</u>	<u>250</u>	JSR SRCHTB	go search table
45	14		BCS MATCH	if carry is set a match was found
206	77		LDA A #'?'	} no match found print '?'
275	374	270	JSR CHAR	
275	<u>361</u>	<u>77</u>	JSR CHECKX	terminates sequence will not return here

MATCH

336	342	LDX TEMAX	} Branch to vector specified in the table
356	4	LDX 4, X	
158	0	JMP 0, X	



\*\*\* CHECK Subroutine \*\*\*

(514) 1704 60<sub>8</sub>

CHECK	226	340	LDA A TEMP	} was last character typed a CR
	201	15	CMA A #)CR	
	46	5	BNE CHECKY	if not - branch
	175	2 0	TST STRDNG	any characters in STRDNG?
	47	4	BEQ CHECKX	if not - branch to terminate
170473 <sub>8</sub>				
CHECKY	201	12	CMA A #)LF	a LF?
	46	15	BNE END	if not - return

(516) 170477<sub>8</sub>

CHECKX	316	<u>361</u>	<u>0</u>	LDX # CRSCH	} Terminate current sequence
	337	262		STX NJSR	
	316	<u>361</u>	<u>115</u>	LDX # PCL*	} print and control
	275	376	220	JSR PRINT	
	62			PULA	} remove one return level
	62			PULA	
END	71			RTS	return to caller

PCL\* 15 12 / CR LF \* N\_u /

52 0

29 July 78

Apo



\* \* \*      Get STRMG Subroutine      \* \* \*

170530<sub>8</sub>  
(5260)  
GETSTR

62		PULA	} Save return Address until completion of String Acquisition
227	350	STA RVEC	
62		PULA	
227	351	STA RVEC+1	
316	<u>361</u>	<u>161</u>	} set up transfer Vector
337	262	STX NJSR	

LOOP

316	2	120	LDX #STRMG+80.	get top of string
11			DEX	Pop pointer
157	0		CLR 0,X	clear string
214	2	0	CPX #STRMG	at buffer yet?
46	370		BNE LOOP	if not - go clear more
337	346		STX IVX	String pointer
71			RTS	return 2 levels back

170561<sub>8</sub>  
(5244)  
GETS

\* \* \*      Enter here to load string      \* \* \*

201	15		cmp A #5CR	acarnage return?
47	4		BEQ END	
201	12		cmp A #5LF	e Line feed?
46	7		BNE CNT	if not - branch
174	0	341	inc TEMPB	} else return to line, after load JSR GETSTR ; inhibit character print
336	350		LDX RVEC	
156	0		jmp 0,X	

END

CONT

OU

DEL

EXT



CONT	336	346	LDX	IVX	pointer to string position
	201	177	CMP	A #'DEL	} is this a 'delete' character
	47	13	BEQ	DELETE	
	247	0	STA	A 0, X	else same character
	214	2116	CPX	# STACK + 78.	are we at end of buffer?
	47	3	BEQ	OUT	yes - branch
	10		INX		else update pointer
	337	346	STX	IVX	
OUT	71		RTS		Return to caller
DELETE	11		DEX		decrement pointer
	214	1 377	CPX	# STACK - 1	are we below bottom of stack?
	47	11	BEQ	EXIT	yes - Exit
	337	346	STX	IVX	else update pointer
	157	0	CLR	0, X	delete character
	206	134	LOA	A #'/'	get character
	275	374 270	JSR	CHAR	print 'Delete' mark
EXIT	174	0 341	INC	TEMPB	inhibit reprint
	71		RTS		Return to Caller

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ADD







(See patch 19 Sept 78 pg 63)

(5640) 171 320<sub>ε</sub>

SYST	316	376	240	LDX	"SYSTD	} Print Test Rom ID
	275	376	220	JSR	PRINT	

SYSTB	316	<u>361</u>	<u>337</u>	LDX	"SYSTM	} PRINT MONITOR ID
	275	376	220	JSR	PRINT	

	275	<u>361</u>	<u>77</u>	JSR	CHECKX	Terminate Sequence (will not return here)
--	-----	------------	-----------	-----	--------	--

(5670) 171 337<sub>g</sub>

SYSTEM	15	12	40	62
	71	40	112	125
	114	131	40	61
	71	67	70	40
	55	40	114	117
	103	101	114	40
	115	117	116	111
	124	117	122	40
	122	117	115	15
	12	52	∅	

/<sup>c</sup>R<sup>L</sup>F<sup>s</sup> 29<sup>s</sup> JULY<sup>s</sup> 1978

<sup>s</sup>P<sup>-</sup><sup>s</sup>P LOCAL<sup>s</sup> MONITOR<sup>s</sup>

ROM<sup>c</sup>R<sup>L</sup>F \* N<sub>u</sub> /



\*\*\*

## Response Message

\*\*\*

171020<sub>8</sub>

(6040)

```

MSG      316  362  27  LDX  #PRES
          275  376  220 JSR  PRINT
          71          RTS

```

PRES

15 12

/ <sup>C</sup> <sub>R</sub> <sup>L</sup> \* <sup>S</sup> <sub>P</sub> >> <sup>S</sup> <sub>P</sub> <sup>S</sup> <sub>P</sub> NUM /

52 40

76 76

40 40  $\phi$ 

\*\*\*

TIME

SUBROUTINE

\*\*\*

TIME					
275	<u>362</u>	<u>20</u>	JSR	MSG	Print message
275	<u>361</u>	<u>130</u>	JSR	GETSTR	get string
275	<u>361</u>	<u>60</u>	JSR	CHECK	check termination
316	40	70	LDX	# 40 70	} 'To' address
337	360		STX	360	
316	2	0	LDX	# STRING	} 'from' address
337	362		STX	362	

L00

END



	206	13	LDA A #13 <sub>8</sub>	allow only 11 characters
	137		CLR B	character count
LOOP	155	0	TST 0, X	} is this the end of the string - go to END
	47	5	BEQ END	
	134		INC B	one more character
	10		INX	increment pointer
	112		DEC A	more allowed
	46	367	BNE LOOP	yes - loop
END	327	364	STA B \$F4	save character count
	275	373 320	JSR FILL	go load time
	275	<u>361</u> <u>77</u>	JSR CHECKX	Terminate Sequence



\* \* \*

## CTLC Routine

\* \* \*

(6260) 171130g

CTLC	275	<u>362</u>	<u>20</u>	JSR MSG	Print Message
	275	<u>361</u>	<u>130</u>	JSR GETSTR	Get character string
	275	<u>361</u>	<u>60</u>	JSR CHECK	check termination
	266	2	0	LDA A STRDIO	} a Y typed?
	201	131		CMP A #SY	
	46	3		BNE NO	if not - go to NO
	117			CLRA	clear A
	402			BR END	branch
NO	206	40		LDA A #40	set A = 40
END	227	234		STA A DCTL	update DCTL
	275	<u>361</u>	<u>77</u>	JSR CHECKX	Terminate sequence

\* \* \*

## Printing Subroutine

\* \* \*

(6360) 171170g

PCL*A	316	<u>362</u>	<u>177</u>	LDX #CL*A
	275	376	220	JSR PRINT
	71			RTS
CL*A	15	12		/ <sup>C</sup> <sub>R</sub> <sup>L</sup> <sub>F</sub> * A <sup>N</sup> <sub>u</sub> /
	52	101	0	

(6420)

(6460)

SHFT

(6520)

PBYTE

PWORD

GO



(6420) 171 210<sub>8</sub>

PSP	316	362	217	LDX	*SP
	275	376	220	JSR	PRINT
	71			RTS	

SP	40	40		/	s <sub>p</sub>	s <sub>p</sub>	s <sub>p</sub>	N <sub>u</sub> /
	40	∅						

\* \* \* PRINT OCTAL VALUE Subroutine \* \* \*

(6460) 171 230<sub>8</sub>

SHFT	170	0	355	ASL	VL	} 24 Bit shift
	171	∅	354	ROL	VH	
	171	∅	356	ROL	VAL	
	71			RTS		return to caller

(6520) 171 250<sub>8</sub>

PBYTE	206	3	LDA	A #3	count of 3 characters	
	40	2	BR	GO	branch go	
PWORD	206	6	LDA	A #6	count of 6 characters	
GO	356	∅	LDX	∅, X	} get binary value to convert to octal	
	337	354	STX	VH		
	66		PSH	A	Same character count	
	177	∅	356	CLR	VAL	clear character shift
	201	6	CMP	A #6	is count = 6	
	47	3	BEQ	6C	if yes - do 6 characters	

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ARD



3C	275	<u>362</u>	<u>230</u>	JSR	SHFT	shift a bit	(6660)
6C	275	<u>362</u>	<u>230</u>	JSR	SHFT	shift a bit	EVA
	206	60		LDA	A #30	} develop a character from shifted data	
	233	356		ADD	A VAL		
	275	374	270	JSR	CHAR	Print character	LOOP
	62			PULA		get count	
	112			DECA		decrement count	
	47	11		BEQ	EXIT	if count = 0, finished	
	66			PSH	A	save count	LOOP
	177	0	356	CLR	VAL	clear character	
	275	<u>362</u>	<u>230</u>	JSR	SHFT	shift a bit	
	40	346		BR	3C	go finish shifting	
EXIT	71			RTS		Return to Caller	

OUT



\*\*\* EVALUATE STRING TO OCTAL CONSTANT \*\*\*

(6660) 171 330<sub>8</sub>

EVAL 177 0 355  
177 0 354

CLR VL  
CLR VH

} clear constant to zero

LOOP1 246 0

LDA A 0, X

get first character

47 25

BEQ OUT

if = 0 finished with input

200 60

SUB A \* 0

compute binary value

306 3

LDA B #3

set to shift current value by 3 bits

LOOP2 275 362 230

: JSR SHFT

go shift

132

DEC B

finished yet?

46 372

BNE LOOP2

No - Loop2

233 355

ADD A VL

331 354

ADC B VH

} compute updated

227 355

STA A VL

value

327 354

STA B VH

14

INX

point to next character

40 347

BR LOOP1

go loop1

OUT 71

RTS

Return to Caller



\*\*\* Print Address Routine \*\*\*

(6770) 171 374<sub>8</sub>

PRINT ADD	275	<u>362</u>	<u>170</u>	JSR	PCL * A	Print Address Prefix
	316	0	344	LDX	#PKX	} Print word
	275	<u>362</u>	<u>254</u>	JSR	PWORD	
	71			RTS		Return to Caller

\*\*\* PRINT DAT \*\*\*

(7020) 171 410<sub>8</sub>

PRINT DAT	275	<u>362</u>	<u>210</u>	JSR	PSP	Print space
	336	344		LDX	PKX	} increment pointer
	10			INX		
	337	344		STX	PKX	
	11			DEX		point to last byte
	275	<u>362</u>	<u>250</u>	JSR	PBYTE	Print Byte
	71			RTS		Return to Caller

\*\*\* STRING EVALUATION \*\*\*

(7060) 171 430<sub>8</sub>

PEVAL	316	2	0	LDX	#STRING	} go evaluate string
	275	<u>362</u>	<u>330</u>	JSR	EVAL	
	336	354		LDX	VH	} some evaluation
	337	344		STX	PKX	
	71			RTS		Return to Caller



	* * *	PEEK	SUBROUTINE	x x y
(710)	171450 <sub>8</sub>			
PEEK	275	<u>362</u>	<u>20</u>	JSR MSG Print input designator
	275	<u>361</u>	<u>130</u>	JSR GETSTR get Address
	275	<u>361</u>	<u>60</u>	JSR CHECK check string Termination
	275	<u>363</u>	<u>30</u>	JSR PEVAL evaluate string
PADD	275	<u>362</u>	<u>374</u>	JSR PRNT ADD print Address
	206	14		LDA A #14 <sub>8</sub> set up a count of 8
LOOP	66			PSH A save count
	275	<u>363</u>	<u>10</u>	JSR PRNT DAT go print first byte
	62			PULA
	112			DECA } decrement counter
	46	370		BNE LOOP if not Loop
	275	<u>361</u>	<u>130</u>	JSR GETSTR get character
	275	<u>361</u>	<u>73</u>	JSR CHECKY check for termination
	40	353		BR PADD if not go print another 8 Bytes!

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ARD



	* * *	POKE ROUTINE	* * *	
(7240)	177 520 <sub>8</sub>			
POKE	275	<u>362</u> <u>20</u>	JSR MSG	Print input designator
	275	<u>361</u> <u>130</u>	JSR GETSTR	get address
	275	<u>361</u> <u>60</u>	JSR CHECK	check string termination
	275	<u>363</u> <u>30</u>	JSR PEVAL	evaluate Address
AGAIN	275	<u>362</u> <u>374</u>	JSR PRINTADD	print Address
	275	<u>362</u> <u>210</u>	JSR PSP	print spaces
	275	<u>361</u> <u>130</u>	JSR GETSTR	get new data
	275	<u>361</u> <u>73</u>	JSR CHECKY	check termination
	316	2 $\emptyset$	LDX "STRING	} evaluate data
	275	<u>362</u> <u>330</u>	JSR EVAL	
		226 355	LDA A VL	
		336 344	LDX PKX	'poke' address!
		175 2 $\emptyset$	TST STRING	if string $\neq 0$
		47 2	BEQ NOUP	then poke value
		247 $\emptyset$	STA A $\emptyset, X$	into position
Noup		10	INX	increment position
		337 344	STX PKX	update pointer
		40 336	BR AGAIN	go loop Again

7400  
BAUD

7460  
PCL

7500  
PUT

LEAD



* * *	BAUD	ROUTINE	* * *
7400	171600g		
BAUD	275 <u>362</u> <u>20</u>	JSR MSG	print input designator
	275 <u>361</u> <u>130</u>	JSR GETSTR	get input string
	275 <u>361</u> <u>60</u>	JSR CHECK	check termination
	275 <u>363</u> <u>30</u>	JSR PEVAL	evaluate string
	226 355	LDA A VL	} store evaluation in NULL
	227 221	STA A NULL	
	275 <u>361</u> <u>77</u>	JSR CHECKX	Terminates Sequence

* * *	OTHER	PRINTING ROUTINES	* * *
7460	171630g		
PCLX	316 <u>361</u> <u>115</u>	LDX # PCLX	
	275 376 220	JSR PRINT	
	71	RTS	

7500	* * *	PUT	Value	Routine	* * *
	171640g				
PUT	175 2 $\phi$	TST STRING		any characters in string?	
	47 15	BEQ END		no - finished	
	316 2 0	LDX # STRING		} evaluate string	
	275 <u>362</u> <u>330</u>	JSR EVAL			
	336 344	LDX PKX		} point address to constant location	
	11	DEC X			
	226 355	LDA A VL		} place value in location	
	247 $\phi$	STA A $\phi, X$			
END	71	RTS		Return to Caller	328-78 ARD



\*\*\*

## MASH LISTINGS

\*\*\*

780 1716708

10040

PIBIS	316	<u>363</u>	<u>340</u>	LDX #LIBIS	point to print string
	40	15		BR GO	go
PIBIC	316	<u>363</u>	<u>351</u>	LDX #LIBIC	point to print string
	40	16		BR GO	go
POBIS	316	<u>363</u>	<u>362</u>	LDX #LOBIS	point to print string
	40	3		BR GO	go
POBIC	316	<u>363</u>	<u>373</u>	LDX #LOBIC	point to print string
GO	337	346		STX IVX	same pointer
	275	<u>363</u>	<u>230</u>	JSR PCLX	print CLX
	275	<u>362</u>	<u>210</u>	JSR PSP	space
	336	346		LDX IVX	} print string
	275	376	220	JSR PRINT	
	275	<u>363</u>	<u>10</u>	JSR PRINT DAT	print current data
	275	<u>362</u>	<u>20</u>	JSR MSG	print input designator
	74			RTS	Return to Caller

LIBIS 40 111 102 111  
123 40 40 75  $\phi$

$$/ \begin{matrix} s_p \\ s_p \end{matrix} \text{IBIS} \begin{matrix} s_p \\ s_p \end{matrix} = N_u /$$

LIBIC 40 111 102 111  
103 40 40 75  $\phi$

$$/ \begin{matrix} s_p \\ s_p \end{matrix} \text{IBIC} \begin{matrix} s_p \\ s_p \end{matrix} = N_u /$$

LOBIS 40 117 102 111  
123 40 40 75  $\phi$

$$/ \begin{matrix} s_p \\ s_p \end{matrix} \text{OBIS} \begin{matrix} s_p \\ s_p \end{matrix} = N_u /$$

LOBIC 40 117 102 111  
103 40 40 75  $\phi$

$$/ \begin{matrix} s_p \\ s_p \end{matrix} \text{OBIC} \begin{matrix} s_p \\ s_p \end{matrix} = N_u /$$



\*\* \*

MASH

ROUTINE

\* \*\* \*

50

10040 172020g

MASH	316	0	230	LDX	# IBIS	} pointer for PUT routine
	337	344		STX	PKX	
	275	<u>363</u>	<u>270</u>	JSR	PIBIS	print sequence for IBIS
	275	<u>361</u>	<u>130</u>	JSR	GETSTR	get string
	275	<u>361</u>	<u>73</u>	JSR	CHECKY	check termination
	275	<u>363</u>	<u>240</u>	JSR	PUT	put data in place
	275	<u>363</u>	<u>275</u>	JSR	PIBIC	print sequence for IBIC
	275	<u>361</u>	<u>130</u>	JSR	GETSTR	get string
	275	<u>361</u>	<u>73</u>	JSR	CHECKY	check Termination
	275	<u>363</u>	<u>240</u>	JSR	PUT	Put data in place
	275	<u>363</u>	<u>302</u>	JSR	POBIS	print sequence for OBIS
	275	<u>361</u>	<u>130</u>	JSR	GETSTR	get string
	275	<u>361</u>	<u>73</u>	JSR	CHECKY	check Termination
	275	<u>363</u>	<u>240</u>	JSR	PUT	put data in place
	275	<u>363</u>	<u>307</u>	JSR	POBIC	print sequence for OBIC
	275	<u>361</u>	<u>130</u>	JSR	GETSTR	get string
	275	<u>361</u>	<u>73</u>	JSR	CHECKY	check Termination
	275	<u>363</u>	<u>240</u>	JSR	PUT	put data in place
	275	<u>361</u>	<u>77</u>	JSR	CHECKX	Terminate sequence

30 July 78  
ABD



\*\*\*

LOADER

SUBROUTINES

\*\*\*

10240

1721208

ADDR

246 6

LDA A 6,X

} mask everything except

204 3

AND A #3

C1 controls

247 6

STA A 6,X

347 4

STA B 4,X

set direction for output

212 74

ORA A #74

C2 controls + Data Access

247 6

STA A 6,X

set control

10

INX

increment pointer

71

RTS

Return to Caller

DATA

246 10

LDA A 10,X

} mask everything except

204 3

AND A #3

C1 controls

247 10

STA A 10,X

347 6

STA B 6,X

set for output

212 4

ORA A #4

Data access bit

247 10

STA A 10,X

set control

10

INX

increment pointer

71

RTS

Return to Caller

DAT



DATI	246	12	LDA A 12,X	} most everything except CS controls
	204	3	AND A #3	
	247	12	STA A 12,X	
	157	14	CLR A 10,X	
	212	4	ORA A #4	set to access data
	247	12	STA A 12,X	set control
	10		INX	increment pointer
	71		: RTS	Return to Caller

30 July 78  
ARD



\*\*\* SETUP ALL NPR REGISTERS \*\*\*

10400 1722008

10520

SETNPR 316 0 44 LDX # \$24 get Base Address

TRNS

157 3 CLR 3, X clear P control

206 37 LDA A #37 } set direction of

247 1 STA A 1, X } NPR control

206 56 LDA A #56 } Set P control for data

247 3 STA A 3, X } access & CBZ stubs

GO

157 1 CLR 1, X P reset to NOP

306 377 LDA B #377 for data direction use

275 364 120 JSR ADDR set High order Address Byte

275 364 120 JSR ADDR set Low order Address Byte

275 364 140 JSR DATD set High order DATD Byte

275 364 140 JSR DATD set Low order DATD Byte

275 364 160 JSR DATI set High order DATI Byte

275 364 160 JSR DATI set Low order DATI Byte

MORE

71 RTS return to caller



	* * *	TRANSFER	DATA	SUBROUTINE	* * *
10520	172	250			
TRANSFER	336	344	LDX PKX	} get # of <u>words</u> to transfer	
	356	0	LDX 0, X		
	46	1	BNE GO	if = 0 none to transfer	
	71		RTS	Return to Caller transfer Complete	
GO	337	225	STX CNTR	set counter	
	336	344	LDX PKX	} get Transfer Address	
	356	2	LDX 2, X		
	337	50	STX \$28	set NPR Address	
	336	344	LDX PKX	} update PKX to point to first Data word!	
	14		INX		
	14		INX		
	14		INX		
	14		INX		
	337	344	STX PKX		
MORE	336	344	LDX PKX	} get Data word for transfer	
	356	0	LDX 0, X		
	337	54	STX \$2C	Place data at DATA register	
	206	5	LDA A #OPR	} Start NPR operation will be complete within 20 seconds	
	227	45	STA A \$25		

30 July 78  
ABD



336	344	LDX	PKX	} update data pointer
10		INX		
10		INX		
337	344	STX	PKX	

336	50	LDX	\$28	} update NPR Address
10		INX		
10		INX		
337	50	STX	\$28	

336	225	LDX	CNTR	} Decrement counter
11		DEX		
337	225	STX	CNTR	

46 343 BNE MORE     $\neq 0$     more to do

40 312 BR TRANSFER    check for more

10720

BDS

LOA

LOD



\* \* \* LOADING PROGRAM \* \* \*

10720

172 350<sub>8</sub>

BDSK 316 365 40 LDX #BOSKD Boot Disk Data

LOAD 172 353<sub>8</sub>  
337 344 STX PKX Save pointer

275 364 200 JSR SETNPR Set all NPR registers

226 53 LDA A \$2B  
204 367 AND A #367  
227 53 STA A \$2B } Cause a "Power Fail"

LOOP 316 0 0 LDX #0 0 }  
10 INX } .7 sec wait Loop  
46 375 BNE LOOP }

226 52 LDA A \$2A }  
204 367 AND A #367 } Enable forced NPR's  
227 52 STA A \$2A }

275 364 250 JSR TRANSFR go transfer Data

226 52 LDA A \$2A }  
212 10 ORA A #10 } disable forced NPR's  
227 52 STA A \$2A }

226 53 LDA A \$2B }  
212 10 ORA A #10 } Terminate "Power Fail"  
227 53 STA A \$2B } CPU Restarts

30 July 78  
ABD



	275	<u>365</u>	<u>27</u>	JSR	AHEAD	push return address on stack	1100
OUT	275	<u>361</u>	<u>77</u>	JSR	CHECKX	Terminate Sequence	BDSK

173/27  
AHEAD 7 TPA get status register

66	PSHA	} push 5 words on stack
66	PSHA	
66	PSHA	
66	PSHA	
66	PSHA	

176	373	<u>236</u>	JMP	BRKINT	BRKINT will return via an RTI to 'OUT'
-----	-----	------------	-----	--------	--



\*\*\* Floppy Disc Bootstrap Loader \*\*\*

172 440<sub>8</sub>

1100

BDSKL	∅	2	CNT1 (= 2 words)	} Power fail Vector Load Data
	∅	24	PFVECTOR (= 24 <sub>8</sub> )	
	337	300	START Address (= 157700 <sub>8</sub> )	
	∅	340	Priority (= 340 <sub>8</sub> )	
	∅	40	CNT2 (= 32 words)	} Boot Data Header
	337	300	START Address (= 157700 <sub>8</sub> )	

\*\*\* PDP 11 Assembly; Language Boot \*\*\*

INIT	12	2	UNIT∅	CLR R2	Default Boot for Unit ∅ R2 = 2∅ For Unit 1
	125	302	UNIT1	BIS #100247, R2	
	200	247			
	25	301	1∅	Mov #RXC5, R1	put CSR address in R1
	376	170			
	260	211	2∅	BITB R2, @R1	wait for Done
	3	376		BEQ 2∅	
	225	303		Mov #7, R3	Set Sector/Track/loop Counter
	∅	7			
	20	100		Mov R1, R∅	Setup to point at RDAF
	20	220		Mov R2, (R∅)+	load read & go Function
	1	2		BR 4∅	go wait

31 July 78  
ABP



25	310	3\$	MOV *1, @R0	1st Sector / 2nd Track / 3rd Jump /
0	1			
14	203	4\$	ASR R3	step through Sector/Track sequence
207	2		BCS 6\$	Branch to wait if S/T not done
225	311		MOV B (PC)+, @R1	load 'empty buffer' command
222	23	5\$	MOV B @R0, (R3)+	MOV data byte to Memory
60	211	6\$	BIT R2, @R1	wait for TR, DONE, or Error
3	376		BEQ 6\$	
201	356		BMI 1\$	if error - retry
207	366		BCS 3\$	branch if in S/T Loop
213	311		TSTB @R1	is data transfer done?
201	371		BMI 5\$	branch to do more
12	0		CLR R0	set to look at A0
45	310		CMP #240, @R0	is it = 240 <sub>8</sub> ?
0	240			if not go retry
2	347		BNE 1\$	
245	302		CMPB #247, R2	sets C = 1 if unit 1
0	247			
13	100		ADC R0	R0 = 0 unit 0
12	7		CLR PC	R0 = 1 unit 1 go to secondary Boot

\* \* \*

Termination

\* \* \*

0 0

11340

BCASD



\* \* \* Cassette Bootstrap Loader \* \* \*

11340

172560<sub>8</sub>

BCASD

∅ 2

CNT1 (= 2)

∅ 24

PF Address (= 24<sub>8</sub>)

337 300

Start Address (= 157700<sub>8</sub>)

∅ 340

Priority (= 340<sub>8</sub>)

} Power Fail Vector Load

∅ 40

CNT2 (= 40<sub>8</sub>)

337 300

START Address = (157700<sub>8</sub>)

\* \* \* PDP 11 Assembly Language \* \* \*

25 300 CBOOT MOV #177500, R0 R0 Holds address of TAIL

377 100 10 26 April 81 ARD

12 0 CLR (R0) select unit ∅

21 301 RESTART MOV PC, R1 use for PIC

145 301 ADD #TABLE-, R1 R1 holds address of Command Table

0 52

25 302 MOV #375, R2 Memory pointer & Data flag

∅ 375

224 103 MOV B (R1)+, R3 Move Test Bits to R3

224 110 LOOP1 MOV B (R1)+, (R0) Move command from table to TAIL

201 13 BMI DONE if command is negative quit

260 310 LOOP2 BIT B R3, (R0) Test Ready and TR Bits

3 376 BEQ LOOP2 branch if bits not set!

31 July 78 ARD



212	202		INCB R2	advance memory pointer	11640
201	372		BMI LOOP1	if minus - try another command	BABLD
234	12		MOVB 2(R0), (R2)	read Data into memory	
0	2				
240	337		CMPB R3, @H0	first byte should = 2408	
0	0				
3	367		BEQ LOOP2	if equal go read another	
0	0	STOP	HALT	Halt on error	
1	355		BR RESTART	Restart or Continue	
13	310	DONE	TST (R0)	check for error	
201	374		BMI STOP	branch to Halt on error	
12	7		CLR PC	go to secondary bank	

TABLE:

		High Byte	Low Byte
37	240	ILBS + Remind	Ready + Transfer Request
5	15	Read + GO	SPACE FORWARD BLOCK + GO
224	24	Read + ILBS	Read + ILBS

\* \* \*

CASSETTE ENTRY POINT

\* \* \*

11600

1727008

BDSK

316 365 160

LDX "BOSKD

176 364 353

JMP LOAD



\*\*\* Absolute Binary Loader \*\*\*

11640

172 720<sub>8</sub>

BABLD

0 2

CNT1 (=2)

0 24

PF ADD (=24<sub>8</sub>)

377 100

STARTING ADDRESS (=157500)

0 340

Priority = (340<sub>8</sub>)

0 140

CNT2 (=140<sub>8</sub>)

377 100

STARTING ADDRESS = (157500)

\*\*\* PDP-11 Assembly Language \*\*\*

; START of Loader

21 306

L.LD1

MOV PC, SP

Setup stack

51 246

cmp -(SP), -(SP)

to start @ L.LD1 -2

21 305

MOV PC, L.PTR

Get Relocated

145 305

ADD #L.READ-, L.PTR

Start Address of Read Routine

0 112

12 1

CLR L.ADR

Clear the Load Address

27 316

L.LD1B

MOV @#L.SR, @SP

pick up contents of SR and Save

377 170

14 16

ROR @SP

check relocation factor

207 2

BCS L.LD1C

jump if relocation needed

12 16

CLR @SP

use tape address

1 3

BR L.LD2

go do load



14	316	L.LDPC	ASL @SP	check for non zero
2	1		BNE L.LDZ	jump if load address specified in SR
20	11.6		MOV L.ADR, @SP	continue loading from last load

; Look for beginning of a Block

12	∅	L.LDZ	CLR L.CKSM	initialize checksum
11	315		JSR PC, @L.PTR	read a frame
212	303		DECB L.BYT	check for +1
2	374		BNE L.LDZ	loop until +1 found
11	315		JSR PC, @L.PTR	read another frame

; input and save byte count, if count = 6 go process jump

11	367		JSR PC, L.GWRD	get free byte count
∅	74			
21	2		MOV R4, L.BC	
345	302		SUB #4, L.BC	subtract 4 to make byte count correct
∅	4			
45	302		CMP #2, L.BC	was byte count = 6
∅	2			
3	43		BEQ L.JMP	jump if no data ie jump block



11	367		JSR PC, L.GWRD	get load address
0	54			
143	204		ADD @SP, R4	generate actual Address
21	1		MOV R4, L.ADR	and put it into the proper cell

; Read in remainder of Data  
 if the Loader Halts @ L.BAD a checksum error has occurred, R3 will contain the expected checksum, and R4 will contain the deviation.

11	315	L.LD3	JSR PC, @L.PTR	read a frame
4	4		BGE L.LD4	branch if more data remains
213	300		TSTB L.CKSM	if checksum is correct
3	353		BEQ L.LD2	then continue
0	0	L.BAD	HALT	checksum error
1	351		BR L.LD2	press continue to ignore checksum
220	321	L.LD4	MOVB L.BYT, (L.ADR)+	store 8 Bits at a time
1	370		BR L.LD3	then Re-Loop

; input a frame, decrement byte count, and accumulate checksum

35	303	L.READ	MOV L.DEV, L.BYT	get device address in L.BYT
0	152			
212	213		INCB @L.BYT	Select Reader
213	313	L.R1	TSTB @L.BYT	DONE?
200	376		BPL L.R1	NO - Loop

31 July 78  
 ADD



234	303		MOV B 2(L.BYT), L.BYT	get character
0	2			
140	300		ADD L.BYT, L.CKSM	add to checksum
105	303		BIC #177400, L.BYT	mask of junk
377	0			
127	302		DEC L.BC	decrement Byte count
0	207		RTS PC	Return

; assemble one full word of Data

25	267	L.GWRD	MOV (SP)+, L.TMP	save return in temporary
0	52			
11	315		JSR PC, @L.PTR	get one character
20	304		MOV L.BYT, R4	save R3 in temporary
11	315		JSR PC, @L.PTR	get another frame
0	303		SWAB L.BYT	place frame in higher order byte
105	303		BIC #377, L.BYT	and clear lower byte
0	377			
120	304		BIS L.BYT, R4	assemble frames into one word
35	307		MOV L.TMP, PC	Return
0	30			



; check correctness of jump address  
 Halt if address is ODD, Jump if Address is even

11	367	L.JMP	JSR PC, L.GWRD	get possible transfer address
377	346			
11	315		JSR PC, @L, PTR	set checksum
213	300		TSTB L.CHSM	if incorrect
2	340		BNE L.BAD	go to checksum Halt
14	204		ASR R4	get low order bit
206	2		BCC L.JMP2	skip if even
∅	∅		HALT	else Halt
1	276		BR L.LD1B	return to start of loading loop
14	304	L.JMP2	ASL R4	restore register
143	204		ADD @SP, R4	
0	114		JMP @R4	jump to User
∅	∅	L.TMP	.WORD ∅	temporary to save stack space
∅	∅		∅	} blank - bill's
∅	∅		∅	
∅	∅		∅	
∅	∅		∅	
∅	∅		∅	
∅	∅		∅	
∅	∅		∅	



\*\*\* Bootstrap Loader

12540  
BMTD

35	301	START	MWV DEVICE, R1	get device address in R1
0	26			
25	302	LOOP	MWV #, -LOAD+2, R2	get address displacement
0	352			
12	211	ENABLE	INC @R1	enable paper tape reader
213	311	WAIT	TSTD @R1	wait until
200	376		BPL WAIT	frame is available
234	162		MWVB 2(R1), LOAD(R2)	store frame read from tape in memory
0	2			
337	∅			
12	267		INC LOOP+2	increment load address displacement
377	356			
1	365	BRANCH	BR LOOP	go back & read more data
377	160	DEVICE	177560	device address
∅	∅		Termination	

\*\*\* ENTRY Point \*\*\*

12500	173240			
BADL	316	<u>365</u>	<u>320</u>	LDX #BADL
	176	<u>364</u>	<u>353</u>	JMP LOAD



# MAGTAPE LOADER

\* \* \*

\* \* \*

12540 173 2608

BMTD

φ 2  
 φ 24  
 337 76  
 φ 340

CNT7 (=2)  
 PF Address (=248)  
 START Address (=157476)  
 Priority (=3408)

} Power Fail  
 } Vapor Loading

φ 141  
 337 76

CNT2 (=141)  
 START Address (=157476)

\* \* \*

## POP II Assembly Language

φ φ  
 21 306  
 13 346  
 25 300  
 φ 212  
 141 200  
 25 301  
 365 122  
 27 346  
 377 170  
 14 6016  
 207 1

HALT      Set up switch register

31 Jul 78  
 ARO



12 16  
14 316  
12 11  
213 311  
200 376  
14 61  
377 376  
206 375  
25 311  
∅ 17  
213 311  
200 376  
∅ ∅  
27 302  
377 170  
44 121  
20 11  
25 341  
377 362  
25 341  
140 3  
213 311



200 376

40 210

3 10

25 361

377 377

0 2

25 34

140 11

213 311

200 376

1 356

44 20

13 310

3 17

44 121

30 11

143 211

34 41

0 2

25 341

140 3

213 311

31 July 78  
ARR



200 376

13 340

13 311

200 337

22 100

∅ ∅

1 376

30 2

13 340

14 202

207 321

14 302

143 202

∅ 112

∅ ∅

∅ ∅

∅ ∅

∅ ∅

74 352

∅ 200

0 22

200 ∅

0 22

13420  
BMT



12 15  
 11 14  
 11 11  
 101 120

0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1  
 0 1

∅ ∅

Termination

13420 BMT  
 \* \* \* 173 610<sub>s</sub>  
 316 366 266 LDX " BMTD  
 176 364 353 JMP LOAD

31 July 78  
 APD



# MONITOR TABLE

13500 173640<sub>8</sub>

123	131	123	124	TABLE:	/ SYST /
	<u>361</u>	<u>320</u>			# SYST
124	111	115	105		/ TIME /
	<u>362</u>	<u>50</u>			# TIME
103	124	114	103		/ CTLC /
	<u>362</u>	<u>130</u>			# CTLC
120	105	105	113		/ PEEK /
	<u>363</u>	<u>50</u>			# PEEK
120	117	113	105		/ POKE /
	<u>363</u>	<u>120</u>			# POKE
102	101	125	104		/ BAUD /
	<u>363</u>	<u>200</u>			# BAUD
115	101	123	113		/ MASH /
	<u>364</u>	<u>20</u>			# MASH
102	104	123	113		/ BOSH /
	<u>364</u>	<u>350</u>			# BOSH
102	103	101	123		/ BCAS /
	<u>365</u>	<u>300</u>			# BCAS
102	101	102	114		/ BABL /
	<u>366</u>	<u>240</u>			# BABL



102 115 124  $\emptyset$

367 210

122 113 125 123

220  $\emptyset$

/ BMT<sub>u</sub> /

#BMT

/ RKUS /

#RKUS

31 July 78  
ARB



Patch 19 Sept 78 ARP

in SYST: pg 44

171 334

176 360 100 JMP SPTCH: go to Patch

170100g

SPTCH:	376	350	0	LDX	350	0	} check if Rom 2 is there
	214	255	255	CPX	#255	255	
	46	5		BNE	\$PT		} if not - skip
	376	350	6	LDX	350	6	} go do sysls in other banks
	255	0		JSR	0,X		

\$PT	275	361	77	JSR	CHECKX:		terminals operation will not return here!
------	-----	-----	----	-----	---------	--	--



## PAGE MODE ROM

164000g  
350 000

255	255	#255 255	'Here' indicator
<u>350</u>	<u>20</u>	#INET	Init pointer
<u>350</u>	<u>70</u>	#DATA	data scanning pointer
<u>350</u>	<u>110</u>	#SYST	ID pointer
<u>354</u>	<u>320</u>	#PCHK	character processing Sense

350 20

INIT:	316	<u>350</u>	<u>340</u>	LDX #GSCAN	} set up scanning routine
	337	264		STX GJSR	
	206	377		LDA A #377	} go initialize to LOCAL
	227	220		STA A ONLINE	
	275	365	27	JSR AHEAD	
	275	<u>352</u>	<u>211</u>	JSR INTIMI!	go set for SCROLL mode
	206	2		LDA A #2	} go init all remaining bonds
	275	<u>350</u>	<u>130</u>	JSR BUNTST	
\$RPT	40	376		BR \$RPT	} endless loop!
	40	374		BR \$RPT	

19 Sept 78  
A/B



350 70<sub>8</sub>

DATA

336 264

LDX GJSR

} go do designated  
subroutine

255 0

JSR 0, X

206 4

LDA A #4

} go check all remaining  
banks

275 350 130

JSR BNHTST

71

RTS

return to caller

350 130

BNHTST

\$LOOP

SYST

275 350 250

JSR ASYST

print 10

206 6

LDA A #6

} check remaining  
Banks

275 350 130

JSR BNHTST

71

RTS

return to caller



## \* \* \* Bank Testing Routine \* \* \*

350 130

BNMTST	66		PSH A	push low order from order Vector Address into stack
	306	340	LDA B #340	} push High order address
	67		PSH B	
	117		CLR A	} push Address of Beginning of Rom Bank #3
	66		PSH A	
	67		PSH B	

\$LOOP	275	<u>350</u> <u>210</u>	JSR BLOOP	go check bank
	60		TSX:	set stack pointer
	346	0	LDA B 0,X	get bank address
	300	10	SUB B #10	next lower bank
	347	0	STA B 0,X	
	347	2	STA B 2,X	
	53	362	BMI \$LOOP	if still in Rom - Loop
	306	4	LDA B #4	} set up to check RAM area
	347	0	STA B 0,X	
	347	2	STA B 2,X	
	275	<u>350</u> <u>210</u>	JSR BLOOP	go check bank
	62		PUL A	} clear stack
	62		PUL A	
	62		PUL A	
	62		PUL A	
	71		RTS	return to Caller



350 210

BL00P

60		TSX	get stack pointer
356	2	LDX 2, X	get Bank Address
356	0	LDX 0, X	get Home indicator
214	255 255	CPX #255 255	is Rom there?
46	10	BNE BSKP	if not - skip

60		TSX	get stack pointer
356	4	LDX 4, X	get Rom Jump Vector Address
356	0	LDX 0, X	get Vector
255	0	JSR 0, X	go to Bank
71		RTS	Return to Caller

BSKP

306	200	LDA 0 #200	set to last bank
60		TSX	set stack pointer
347	2	STA 0 2, X	} store bank address
347	4	STA 0 4, X	
71		RTS	return to caller

350 250

PSYST

350 260

ID



350 250

PSYST 316 350 260 LDX \*ID point to text  
 275 376 220 JSR PRINT for print ID  
 71 RTS

350 260

ID 15 12 40 61 /<sup>c</sup>R<sup>L</sup>F<sup>S</sup><sub>p</sub> 15<sup>S</sup><sub>p</sub> SEPT<sup>S</sup><sub>p</sub> 1978<sup>S</sup><sub>p</sub> -  
 65 40 123 105 <sup>S</sup><sub>p</sub> PAGE<sup>S</sup><sub>p</sub> MODE<sup>S</sup><sub>p</sub> ROM<sup>S</sup><sub>p</sub> <sup>c</sup>R<sup>L</sup>F<sup>S</sup><sub>p</sub> \* N<sub>h</sub> /  
 120 124 40 61  
 71 67 78 40  
 55 40 120 101  
 107 105 40 115  
 117 104 105 40  
 122 117 115 40  
 15 12 52 0



\* \* \* SCAN ROUTINE \* \* \*

329 340

GSCAN:

326 315 LDA B ESL  
 327 314 STA B ESIL  
 226 340 LDA A TEMPA  
 227 315 STA A ESL

} two character shifting  
 Buffer for finding  $E_c =$

\$G4

201 33 CPA A # '  $E_c$

is new character  $E_c$  ?

46 10 BNE \$G2

if not - branch

301 33 CPA B # >  $E_c$

was previous also  $E_c$  ?

47 3 BEQ \$G1

if so - leave allowing  
 printing of previous  $E_c$  !

174 0 341 INC TEMPA

else inhibit printing of  
 current  $E_c$

\$G1

71 RTS

return to caller

GCTL

\$G2

301 33 CPAB # '  $E_c$

was previous character  $E_c$

47 1 BEQ \$G3

if so - branch

71 RTS

else return allowing  
 character to be printed

NCTL

\$G3

201 75 CPA A # ) =

is new character an = ?

47 5 BEQ \$G4

if so - branch

27 TBA

else print previous  $E_c$

275 374 270 JSR CHAR

print character

71 RTS

return allowing current  
 character to be printed



\$G4	174	Ø	341	INC	TEMPB	inhibit printing of =
	316	<u>351</u>	<u>2Ø</u>	LOX	#GCTL	} set up to transfer to GCTL on next character
	337	264		STX	GJSR	
	71			RTS		return to caller

\*\*\* GCTL Routine \*\*\*

GCTL	226	34Ø		LDA	A	TEMPA	get character
	316	<u>35Ø</u>	<u>34Ø</u>	LDX	#GSCAN		} reset transfer vector
	337	264		STX	GJSR		
	200	60		SUB	A	#3Ø	check that character
	45	4		BCS	NTCL		is between Ø and
	201	12		CMP	A	#12	9 inclusively
	45	21		BCS	GØDD		

NCTL	226	314		LDA	ESH		} print E <sub>c</sub>
	275	374	270	JSR	CHAR		
	226	315		LDA	ESL		} print =
	275	374	270	JSR	CHAR		
	177	Ø	314	CLR	ESH		} clear buffer
	177	Ø	315	CLR	ESL		
	71			RTS			return allowing printing of current character

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ARD



GOOD	174	∅	341	INC TEMPB	inhibit printing	164 54
	26			TAB	} compute 3 * A	351 14
	33			ABA		TABLE
	33			ABA		
	137			CLR B		
	213	<u>140</u>		ADD A	#140	} compute Table Location
	311	<u>351</u>		ADC B	#351	
	227	317		STA A	GTABLE+3	} same Location
	327	316		STA B	GTABLE	
	336	316		LDX	GTABLE	get Location
	246	∅		LDA	∅, X	get # of characters needed
	47	15		BEQ	NONE	if none - branch
	227	313		STA A	GCNT	else same count
	316	<u>351</u>	<u>200</u>	LDX	#GETLST	} setup transfer vector
	337	264		STX	GJSR	
	316	2	120	LDX	#GSTRNG	} Buffer for characters
	337	320		STX	GSTADD	
	71			RTS		
NONE	356	1		LDX	1, X	get jump vector
	156	∅		JMP	∅, X	jump to selected routine



164 540

351 140

TABLE:

∅  
354   210

1  
352   140

∅  
352   150

∅  
352   160

∅  
352   170

∅  
352   300

21  
353   40

2  
353   130

∅  
353   170

∅  
353   210

∅, # RAMSET

1, # GBAUI

∅, # NTBI

∅, # ETBI

∅, # NTMI

∅, # PMI

21, # DEFCHR

2, # MASCUR

∅, # DRAGE

∅, # UPAGE

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x + > GETLST Routine x x x

351 200

GETLST	174	∅	341	INC	TEMPB	inhibit printing
	336		320	LDX	GSTADD	set address of Buffer
	226		340	LDA	TEMPA	get character
	247		∅	STA	A, X	store character
	14			INX		update Buffer address
	337		320	STX	GSTADD	store update
	172	∅	313	DEC	GCNT	enough characters?
	57	1		BLE	GETDON	if so - branch
	71			RTS		else return and wait for next character

GDO:

\$PRNT

\$CR:

\$LF

GETDON	316	<u>350</u>	<u>340</u>	LDX	"GSCAN	} reset jump vector
	337	264		STX	GJSR	
	336	316		LDX	GTABLE	set table location
	356	1		LDX	1, X	set vector address
	156	∅		JMP	∅, X	jump to designated routine

\$FF

\$VT



GDD: 201 40 CPA A # > 40 do we have a control character?  
 45 7 BCS \$CR if so - branch  
 \$PRINT: 336 200 LDX CURLOC else place character  
 247 0 STA A 0,X in array  
 176 353 305 JMP ADD1: go update position

\$CR: 201 15 CPA A # > CR Test for CR  
 46 3 BNE \$LF if not - branch  
 176 353 260 JMP CR: do carriage return

\$LF 201 12 CPA A # > LF Test for LF  
 46 3 BNE \$FF if not - branch  
 176 353 272 JMP ADD80: do a line feed

\$FF 201 14 CPA A # > FF test for FF form feed  
 46 3 BNE \$VT if not - branch  
 176 352 300 JMP PM1: do a new page

\$VT 201 13 CPA A # > VT Test for VT Vertical Tab  
 46 3 BNE \$HT  
 176 353 277 JMP SUB80: back one line



\$HT	201	11	CPA A # ' HT	Test for HT Horizontal Tab	\$BL
	46	3	BNE \$BS	if not - branch	
	176	<u>353</u> <u>305</u>	JMP ADD1:	space ahead 1 position	
\$BS	201	10	CPA A # ' BS	Test for BS backspace	\$CTL
	46	3	BNE \$RS	if not - branch	
	176	<u>353</u> <u>312</u>	JMP SUB1:	back up one space	
\$RS	201	36	CPA A # ' RS	Test for RS Home Cursor	\$DNE
	46	3	BNE \$SUB	if not - branch	
	176	<u>352</u> <u>20</u>	JMP HOME:	go home cursor	
\$SUB	201	32	CPA A # ' SUB	Test for SA Clear screen	
	46	3	BNE \$FS	if not - branch	
	176	<u>352</u> <u>40</u>	JMP CLR:	go clear screen	
\$FS	201	34	CPA A # ' FS	Test for FS go to previous page	
	46	3	BNE \$US	if not - branch	
	176	<u>353</u> <u>170</u>	JMP DPAGE:	go scroll to previous page	
\$US	201	37	CPA A # ' US	Test for US go to next page	
	46	3	BNE \$BL	if not - branch	
	176	<u>353</u> <u>210</u>	JMP UPAGE:	go scroll to next page	



\$BL	201	7	CPA A	H > B <sub>L</sub>	Test for <sup>A</sup> Bell
	46	2	BNE	\$CTL	if not - branch
	227	35	STA A	\$LD A	sound Bell
\$CTL	221	234	CPA A	\$CTL	printing of control characters ?
	45	3	BCE	\$DNE	if not - branch
	176	<u>351</u>	<u>244</u>	JMP	\$PRINT
\$DNE	71		RTS		return to Caller

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ARD



\*\*\* Home Cursor Routine \*\*\*

352 20

HOME:	117	CLR A	} clear position in line to beginning of line	CLR:
	227 326	STA A GPCNT		

352 23

HOME2:	117	CLR A	} set character position to Home	\$2CLR
	227 325	STA A GLCNT + 1		
	227 324	STA A GLCNT		

	336 322	LDX GLBASE	} reposition cursor
	337 200	STX CURLOC	
	71	RTS	Return to Caller



352 40

CLR%	275	<u>352</u>	<u>20</u>	JSR	HOME	position cursor at Home
	275	<u>352</u>	<u>113</u>	JSR	POS	compute Address of first character
\$CLR	336	332		LDX	TCLR	} go clear text line
	275	<u>354</u>	<u>161</u>	JSR	CLINE	
	275	<u>352</u>	<u>100</u>	JSR	NLINE	compute address of next line
	275	<u>354</u>	<u>260</u>	JSR	TMCK	this is a long routine so go check for clock tick
	336	324		LDX	GLCNT	} are we finished
	214	6	100	CPX	#6 100	
	46	356		BNE	& LCLR	if not - loop back
	176	<u>352</u>	<u>20</u>	JMP	HOME	go reset pointer to Home



X \* X NLINE Routine \* \* \*

352 100

NLINE: 206 120 LDA A #120  
 137 CLR B  
 233 325 ADD A GLCNT+1  
 331 324 ADC B GLCNT  
 227 325 STA A GLCNT+1  
 327 324 STA B GLCNT

} update GLCNT to  
 next line

352 16

ETBI

352 17

NTMI

352 113

POS: 316 0 332 LDX #TCLR  
 176 354 13 JMP CURPOS

} go compute address of  
 character - result in TCLR

\* \* X BAUD Rate Routine \* \* X

352 140

GBAUD 226 340 LDA A TEMP A get character  
 227 221 STA A NALL set rate  
 71 RTS return to caller

\$A

INTMI

\* \* X NORMAL TEXT Buffer Init \* \* X

352 150

NTBI 316 57 257 LDX # 57 257  
 337 252 STX CLTOP  
 71 RTS

} all for 4K Text

return to caller



\* \* \* Extended Text Buffer Init \* \* \*

352 160

```

ETBI 316 177 257 LDX #177 257
      337 252 STX CLTOP
      71 RTS

```

} setup for 24KText

\* \* \* Normal Text (Scroll) Mode Initialization \* \* \*

352 170

```

NTMI 226 310 LDA A PAGEM
      46 11 BNE $A
      226 311 LDA A PAGEX
      46 5 BNE $A
      226 312 LDA A PAGEY
      46 1 BNE $A
      71 RTS

```

} check if other modes active  
if not - then already  
in Text mode

return to caller

\$A

```

336 242 LDX CLBASE
337 200 STX CURLOC

```

} reset cursor position

INTMI

```

117 CLR A
227 310 STA A PAGEM
227 311 STA A PAGEX
227 312 STA A PAGEY

```

} turn off all other  
modes

```

316 352 244 LDX #LIST
337 362 STX $F2

```

} set up to transfer data  
to Display Control line



352 225

DOFL 316 40 20 LDX W CONTROL } to Address

337 360 STX \$F4

\$1:

206 24 LDA A #24 } set for 20 characters

227 364 STA A \$F4

176 373 320 JMP FILL go fill & return to call

352 244

L IST 40 123 103 122

/ s; SCROLL s; MODE

117 114 114 40

s; s; s; s; s; s; s; s; Nu /

115 117 104 105

40 40 40 40

40 40 40 40

Ø

\*\*\* PAGE MODE Initialization \*\*\*

352 300

PMI 275 375 100 JSR UPDLIN terminate current line

275 372 310 JSR UPDTXT update Text control

336 242 LDX CLBASE } same new Base

337 322 STX GLBASE



	206	24	LDA A #24	set for 20 lines in new page
\$1:	66		PSHA	same counter
	336	240	LDX CLPNTR	get character count pointer
	206	121	LDA A #121	} set line length to 81 <sub>10</sub> characters
	247	0	STA A 0,X	
	275	375	60 JSR CLEAR	go clear line
	275	375	100 JSR UPDLIN	update line pointer
	275	372	310 JSR UPDTXT	update text control
	275	<u>354</u>	<u>260</u> JSR TMCHK	go check time
	62		PUL A	get counter
	112		DEC A	more to do ?
	56	351	BGT \$1	it so - loop
	336	240	LDX CLPNTR	get one extra line
LOOP	114		INC A	of 1 character
	227	215	STA A CURCNT	} set count = 1
	247	0	STA A 0,X	
	275	375	60 JSR CLEAR	go clear line
	275	372	310 JSR UPDTXT	update Text control
	275	<u>352</u>	<u>20</u> JSR HOME	position cursor at Home

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206	377	LDA A #377		
227	216	STA A UPDFLAG	set update flag	
227	310	STA A PAGEM	indicate in Page Mode	
117		CLR A	} clear remaining modes	
227	311	STA A PAGEX		
227	312	STA A PAGEY		
316	<u>353</u>	<u>13</u>	LDX #PMLST	} point to PMLST
337	362		STX \$F2	
176	<u>352</u>	<u>225</u>	JMP DOFL	do fuel & return

353  
DEFCH

353 13

PMLST	40	120	101	107
	105	40	115	117
	104	105	40	40
	40	40	40	40
	40	40	40	40
	0			

/s PAGE.s MODE

s s s s s s s s s s N /

GLOOP



\*\*\* Define character Routine \*\*\*

353 40

DEFCHAR

316 2120 LDX #GSTRNG } get string address  
 246 0 LDA A 0,X } first character is character to  
 10 INX } be redefined  
 337 320 STX GSTADD } set pointer to first data character  
 137 CLR B } clear B  
 275 353 114 JSR \$\*20 } go multiply BA by 20s

213 0 ADD A #0 } add in Base Address for  
 311 20 ADC B #20 } character RAM  
 227 331 STA A GRAM+1 } same address  
 327 330 STA B GRAMB }

206 20 LDA A #20 } 20s Bytes/character

GLOOP

336 320 LDX GSTADD } get address  
 346 0 LDA B 0,X } load character  
 10 INX } update address  
 337 320 STX GSTADD }  
 336 330 LDX GRAMB } get RAM Address  
 347 0 STA B 0,X } store character  
 10 INX }  
 337 330 STX GRAMB } update RAM Address  
 112 DECA } finished ?  
 56 357 BGT GLOOP } if not - Loop  
 71 RTS } Return to Caller



\*\*\* Selected Multiply Routine \*\*\*

353 114

\$\*20      110      ASL A      } 16 bit shift left  
                  131      ROL B      }

353 116

\$\*10      110      ASL A  
                  131      ROL B

353 120

\$\*4      110      ASL A  
                  131      ROL B

353 122

\$\*2      110      ASL A  
                  131      ROL B

71      RTS

353 1  
 DPage  
 \$LPD

\*\*\* MOVE CURSOR \*\*\*

353 130

moveur      316    2    120      LDX #GSTRNG      get Address of string

246    0      LDA A    0,x      get Row #

137      CLR B

275    353    114      JSR    \$\*20      mult. ply BA by 20s

227    325      STA A    GLCNT+1      } same product in GLCNT

327    324      STA 0    GLCNT      }

275    353    120      JSR    \$\*4      get 100g x Row



233	325	ADD A	GLCNT+1	} compute 1208 * Row	
331	324	ADC B	GLCNT		
227	325	STA A	GLCNT	} same row base	
327	324	STA B	GLCNT		
177	∅	326	CLR	GCNT	initialize Column to ∅
246	1		LDA	A, 1, X	get Column location
176	<u>354</u>	<u>370</u>	JMP	Pmar	go to patch

\* \* \* D Page Routine \* \* \*

353 170

DPage	206	25	LDA	A #25	setup for 21 lines	
\$LPO	316	375	370	LDX	" DMSCL	get address of Dam scroll routine
	275	<u>353</u>	<u>240</u>	JSR	HANDLER	go do scrolling
	275	<u>354</u>	<u>260</u>	JSR	TMCK	check for tick
	112			DEC	A	more to do?
	56	364		BGT	\$LPO	if so - loop until finished
	71			RTS		Return to Caller

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ABD



xxx Scroll Up a page Routine xxx

353 210

UPAGE

206 25

LDA A #25

set for 21 lines

\$LPU

336 240

LDX CLPTR

} are we at some

234 254

CPX PLPTR

} line ?

46 3

BNE \$LAH

if not - go scroll

176 376 140

JMP LCDSP

else lock Display and  
return to caller

\$LAH

316 375 220

LDX #UPSCRL

Address of UP SCRL Routine

275 353 240

JSR HANDLER

go scroll

275 354 260

JSR TMCK

check Time

112

DEC A

are we done ?

56 353

BGT \$LPU

if not - loop until finished

71

RTS

Return to Caller

353 240

HANDLER

66

PSH A

} dummy X register

66

PSH A

66

PSH A

} push A, B into stack

67

PSH B

7

TPA

} push CC's

66

PSH A

156  $\emptyset$

JMP  $\emptyset, X$

go to designated handler

will return at location  
past JSR HANDLER

353 26

CR

353 2

ADD 80

353 27

SUB 80

353 3

ADD 1

353

SUB 1



353 260

CR	226	326	LDA A	GPCNT	} get current column position set count to 0
	137		CLR B		
	327	326	STA B	GPCNT	
	100		NEG A		} negate number
	302	0	SBC B	* 0	} extend sign of result through 0
	40	45	BR	UPDGL:	} go update GLCNT

353 272

ADD 80	206	120	LDA A	#120	} load 80 <sub>10</sub> count
	137		CLR B		
	40	40	BR	UPDGL:	} go update GLCNT

353 277

SUB 80	206	260	LDA A	#260	} load -80 <sub>10</sub> count
	306	377	LDA B	#377	
	40	32	BR	UPDGL:	} go update GLCNT

353 305

ADD 1	206	1	LDA A	#1	} load 1 count
	137		CLR B		
	40	3	BR	UPDGL:	} go update GPCNT & GLCNT

353 312

SUB 1	206	377	LDA A	#377	} load -1 count
	26		TAB		



353 315

UPDGP1	66		PSH A	same count
	233	326	ADDA GPCNT	add in count
	52	6	BPL \$U1	if still positive - branch
	213	120	ADDA #120	else add 120 <sub>g</sub>
	40	6	BR \$U2	go table 2

\$UX	200	120	SUB A #120	
\$U1	201	120	CMPA #120	post 120?
	52	372	BPL \$UX	if so - branch
\$U2'	227	326	STA A GPCNT	store result in GPCNT
	62		PUL A	restore count

353 337

UPDGL	233	325	ADDA GLCNT+1	} add in current position
	331	324	ADC B GLCNT	
\$U3	227	325	STA A GLCNT+1	} same result
	327	324	STA B GLCNT	
	52	6	BPL \$U4	if result positive - branch
	213	100	ADDA #100	} else add in graphic buffer length
	311	6	ADC B #6	
	40	364	BR \$U3	go same result

\$U4

PAST

354 13

CURPOS







200	120	SUBA	#120	} is position post end of Buffer
302	0	SBC B	#0	
220	277	SUBA	DVBASE+1	
322	276	SBC B	DVBASE	

354 70  
RPAGE

\$LLP

53 10 BME \$END if not - branch

233	251	ADD A	CLBOT+1	} else add in Base
331	250	ADC B	CLBOT	

247	1	STA A	1, X	} same position
347	0	STA 0	4, X	

\$L570

\$ END 71 RTS return to caller







62		PUL A	get counter
112		DEC A	move to do
56	347	BGT \$LSD	Loop until line done

275	<u>354</u>	<u>260</u>	JSR TMCK	go check time
-----	------------	------------	----------	---------------

336	324	LDX GLCNT	
-----	-----	-----------	--

214	5	360	CPX #5 360	to last line?
-----	---	-----	------------	---------------

46	322	BNE \$LLP	loop until there
----	-----	-----------	------------------

316	0	200	LDX #CURLOC	} go position cursor at beginning of last line
275	<u>354</u>	<u>13</u>	JSR CURPOS	

336	200	LDX CURLOC	Cursor location
-----	-----	------------	-----------------

206	120	LDA A #120	80 characters plus
-----	-----	------------	--------------------

306	40	LDA B #40	Space Code
-----	----	-----------	------------

\$80CL	347	0	STA B 0, X	} clear last line
	10		INX	
	112		DEC A	
	56	372	BGT \$80CL	

71		RTS	Return to Caller
----	--	-----	------------------

354  
RAMSE

\$LOOP



\*\*\* RAM Character Restoration \*\*\*

354 210

RAMSET 316 17 377 LDX #17 377 } Beginning of Character RAM -1

337 374 STX \$FC

316 177 377 LDX #177 377 } Beginning of ROM -1

337 376 STX \$FE

\$LOOP 275 354 260 JSR TMCK check time

336 376 LDX \$FE

10 INX .

337 376 STX \$FE

246 0 LDA A 0,X

336 374 LDX \$FC

10 INX

337 374 STX \$FC

247 0 STA A 0,X

214 37 377 CPX #37 377 finished

416 352 BNE \$LOOP loop until finished

71 RTS return to caller

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ABO



\*\*\*

# Time Check Routine

\*\*\*

354 260

Tmck	175	0	42	TST	\$22	test for IRQ Flag
	53	1		BMI	\$TM	if no tick then
	71			RTS		return to caller
\$TM	66			PSH	A	save A
	172	0	223	DEC	DVAL	assembled ?
	46	6		BNE	TmckDN	no-branch
	206	74		LDA	A #74	} reset counter
	227	223		STA	A DVAL	
	227	213		STA	A TFlag	set Isec Flag
TmckDN	226	40		LDA	A \$20	clear Clock Flag
	62			PUL	A	restore A
	71			RTS		return to caller

354 3:

PCHK

\$K

354 3

PCKRTN



\* \* \*

## Process character check Handler \* &amp; r

80

354 320

PCHK	175	4	316	TST	PAGEM	in Page mode
	47	3		BEQ	\$K	if not - branch
	176	<u>351</u>	<u>240</u>	JMP	BDD	go to page mode Handler

\$K	66			PSH	A	save A
	206	14		LDA	A #14	} check all remainings } Banks
	275	<u>350</u>	<u>130</u>	JSR	BNKTEST	
	62			PUL	A	restore A
	176	377	135	JMP	PATRN	return to CHAR scroll mode

354 350

PCKRTN	61			INS		} Pop return to BLOOP
	61			INS		
	61			INS		} Pop return to BNKTEST
	61			INS		
	61			INS		} Pop Bank Test Vectors
	61			INS		
	61			INS		
	61			INS		
	61			INS		} pop Return to PCHK
	61			INS		
	61			INS		pop character
	71			RTS		Return to Caller of CHAR



\*\*\* Patch of Max Cur

354 370

Pmas	137		CLR B	clear B
	66		PSH A	save A
	52	4	BPL \$B	if positive - branch
\$C	200	120	SUBA#120	else decrease by one line
	53	374	BMI \$C	
\$B	176	<u>353</u>	<u>316</u>	JMP UPDGP+1



## Software patches

in CLKD 174200g

change of clock control register

174211g

227 26 STA A \$16

226 24 LDA A \$14

in CLKINT 174300g

change of clock control registers

174331g

226 24 LDA A \$14

20 Mar 1979

ARD



# Software Changes

in TIMCLK 1743408

From ABOVE! (changes indicated by \* )  
corrects Am/pm changes

ABOVE! 206 62 LDA A 12'2

241 1 CMP A 1,X

\* 56 22 BGT TIMDON.

\* 47 3 BEQ DYNT

\* 40 17 BR 13th Hr

\*  $\emptyset$  - - -

DYNT 206 101 LDA A 11'A

241 9 CMP A 9,X

47 3 BEQ NITE

247 11 STA A 9,X

71 RTS

NITE 206 120 LDA A 11'P

247 11 STA A 9,X

TIMDON 71 RTS

\* 13th Hr 347  $\emptyset$  STA B  $\emptyset$ ,X

\* 134 INC B

\* 347 1 STA B 1,X

\* 71 RTS



# Key Board Utility System Rom's

110660<sub>8</sub>

RKUS 316 220 24 LDX \*KUS  
 176 364 353 JMP LOAD

110624<sub>8</sub>

0	2	CNT1	length of first segment = 2 words
0	24	PF Vector	Address for transfer
320	0	150000 <sub>8</sub>	HUS starting location
0	340	340 <sub>8</sub>	Priority at start

7	200	CNT2	length of HUS 3600 <sub>8</sub> words
320	0	START	Start address

HUS Text 3600<sub>8</sub> words

0 0 Termination

28 Sept 78  
 AD